

DATASHEET

DLPCA-D-S1

Dual Channel Low Noise Current Amplifier



Electro Optical Components, Inc.

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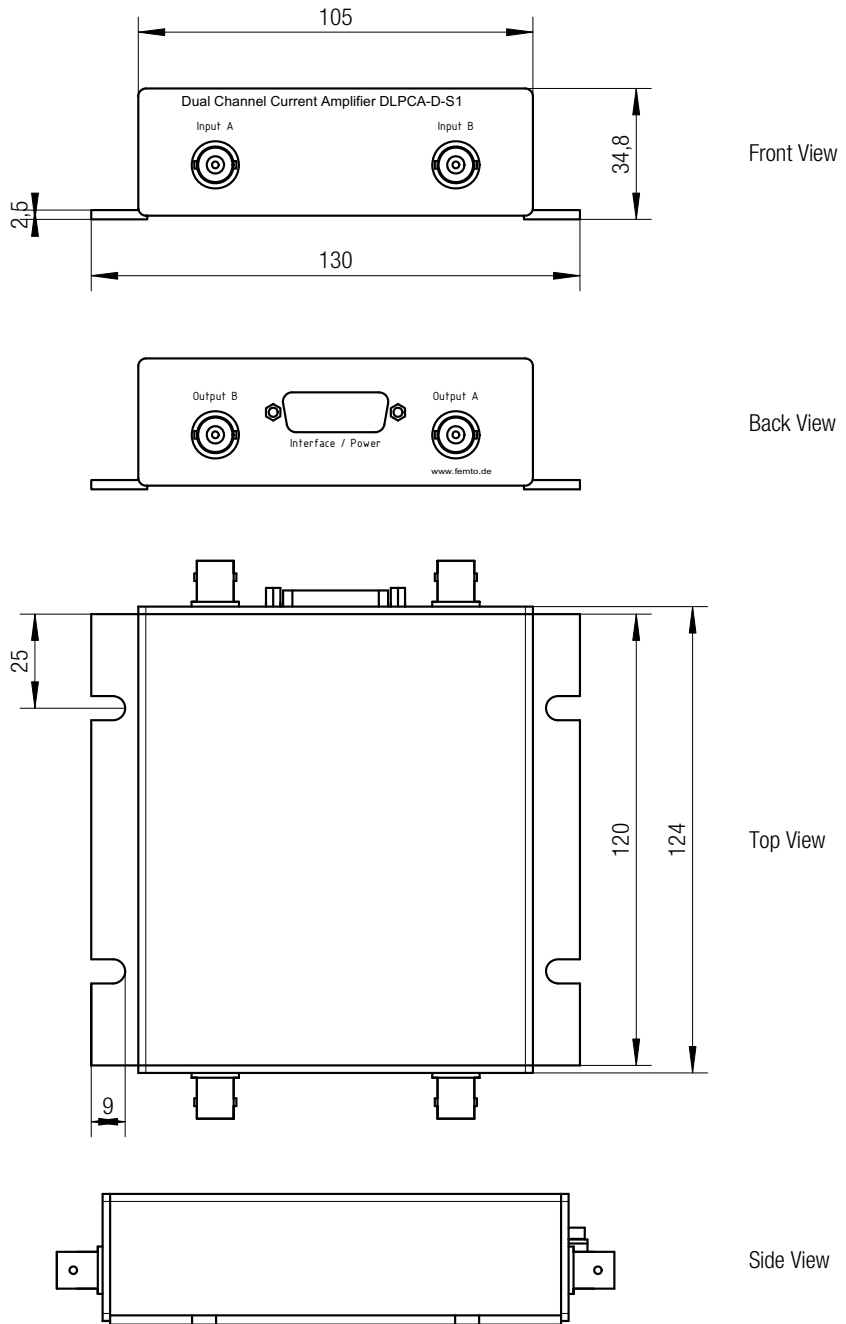
Features	<ul style="list-style-type: none"> • Two Separate Channels in One Compact Housing • Transimpedance (Gain) 1×10^5, 1×10^7 and 1×10^9 V/A • Transimpedance (Gain) Individually Switchable for Channels A and B by Opto-Isolated Control Interface • Bandwidth DC ... 2 kHz • Fast Switching Time of Typically 1 ms between Gain Settings • Protection Against ± 3 kV Transients 																																																																																																																																																				
Applications	<ul style="list-style-type: none"> • Dual Channel Photodiode Amplifier • Spectroscopy • Beam Monitoring for Particle Accelerators / Synchrotrons • Ionisation Detectors 																																																																																																																																																				
Specifications	<p><i>Test Conditions</i> <i>V_s = ± 15 V, T_a = 25°C</i></p> <table border="0"> <tr> <td style="vertical-align: top;">Gain</td> <td>Transimpedance</td> <td colspan="3">1×10^5, 1×10^7 and 1×10^9 V/A</td> </tr> <tr> <td></td> <td>Gain Accuracy</td> <td colspan="3">± 2 %</td> </tr> <tr> <td></td> <td>Linearity</td> <td colspan="3">typ. < 0.1 %</td> </tr> <tr> <td></td> <td>Gain Drift</td> <td colspan="3">see table below</td> </tr> <tr> <td></td> <td>Switching Time</td> <td colspan="3">1 ms typ. for gain increase/decrease</td> </tr> <tr> <td style="vertical-align: top;">Frequency Response</td> <td>Lower Cut-Off Frequency</td> <td colspan="3">DC</td> </tr> <tr> <td></td> <td>Upper Cut-Off Frequency</td> <td colspan="3">up to 2 kHz (see table below)</td> </tr> <tr> <td style="vertical-align: top;">Input</td> <td>Equ. Input Noise Current</td> <td colspan="3">see table below (value per $\sqrt{\text{Hz}}$, @ 100 Hz)</td> </tr> <tr> <td></td> <td>Equ. Input Noise Voltage</td> <td colspan="3">4 nV/$\sqrt{\text{Hz}}$ (@ 100 Hz)</td> </tr> <tr> <td></td> <td>Input Bias Current</td> <td colspan="3">1 pA typ.</td> </tr> <tr> <td></td> <td>Max. Input Current</td> <td colspan="3">see table below (value for linear amplification)</td> </tr> <tr> <td></td> <td>Input Offset</td> <td colspan="3">< 1 mV for all gain settings</td> </tr> <tr> <td></td> <td>Input Offset Drift</td> <td colspan="3">< 20 $\mu\text{V}/^\circ\text{C}$</td> </tr> <tr> <td></td> <td>Crosstalk between Channels</td> <td colspan="3">better -90 dB</td> </tr> <tr> <td style="vertical-align: top;">Performance depending on Gain Setting</td> <td>Gain Setting</td> <td>10^5 V/A</td> <td>10^7 V/A</td> <td>10^9 V/A</td> </tr> <tr> <td></td> <td>Upper Cut-Off Frequency (-3 dB)</td> <td>2 kHz</td> <td>2 kHz</td> <td>1.5 kHz</td> </tr> <tr> <td></td> <td>Rise / Fall Time (10% - 90%)</td> <td>180 μs</td> <td>180 μs</td> <td>240 μs</td> </tr> <tr> <td></td> <td>Equ. Input Noise Current ($\sqrt{\text{Hz}}$)</td> <td>500 fA</td> <td>45 fA</td> <td>4.5 fA</td> </tr> <tr> <td></td> <td>Output Noise (peak-peak)</td> <td>< 1 mV</td> <td>< 1 mV</td> <td>2 mV</td> </tr> <tr> <td></td> <td>Gain Drift ($^\circ\text{C}$)</td> <td>0.01%</td> <td>0.01%</td> <td>0.02%</td> </tr> <tr> <td></td> <td>Max. Input Current (\pm)</td> <td>100 μA</td> <td>1 μA</td> <td>10 nA</td> </tr> <tr> <td></td> <td>DC Input Impedance (\parallel 5 pF)</td> <td>50 Ω</td> <td>200 Ω</td> <td>10 kΩ</td> </tr> <tr> <td style="vertical-align: top;">Output</td> <td>Output Voltage</td> <td colspan="3">± 10 V (@ > 10 kΩ load)</td> </tr> <tr> <td></td> <td>Output Impedance</td> <td colspan="3">50 Ω (terminate with > 10 kΩ load for best performance)</td> </tr> <tr> <td></td> <td>Max. Output Current</td> <td colspan="3">± 20 mA</td> </tr> <tr> <td></td> <td>Output Offset</td> <td colspan="3">< 1 mV for all gain settings (no signal)</td> </tr> <tr> <td></td> <td>Output Offset Drift</td> <td colspan="3">< 20 $\mu\text{V}/^\circ\text{C}$</td> </tr> <tr> <td style="vertical-align: top;">Digital Control</td> <td>Control Input Voltage Range</td> <td colspan="3">Low: -1 ... +1 V, High: +3 ... +12 V</td> </tr> <tr> <td></td> <td>Control Input Current</td> <td colspan="3">0 mA @ 0 V, 1.8 mA @ +5 V, 5 mA @ +12 V</td> </tr> </table>				Gain	Transimpedance	1×10^5 , 1×10^7 and 1×10^9 V/A				Gain Accuracy	± 2 %				Linearity	typ. < 0.1 %				Gain Drift	see table below				Switching Time	1 ms typ. for gain increase/decrease			Frequency Response	Lower Cut-Off Frequency	DC				Upper Cut-Off Frequency	up to 2 kHz (see table below)			Input	Equ. Input Noise Current	see table below (value per $\sqrt{\text{Hz}}$, @ 100 Hz)				Equ. Input Noise Voltage	4 nV/ $\sqrt{\text{Hz}}$ (@ 100 Hz)				Input Bias Current	1 pA typ.				Max. Input Current	see table below (value for linear amplification)				Input Offset	< 1 mV for all gain settings				Input Offset Drift	< 20 $\mu\text{V}/^\circ\text{C}$				Crosstalk between Channels	better -90 dB			Performance depending on Gain Setting	Gain Setting	10^5 V/A	10^7 V/A	10^9 V/A		Upper Cut-Off Frequency (-3 dB)	2 kHz	2 kHz	1.5 kHz		Rise / Fall Time (10% - 90%)	180 μs	180 μs	240 μs		Equ. Input Noise Current ($\sqrt{\text{Hz}}$)	500 fA	45 fA	4.5 fA		Output Noise (peak-peak)	< 1 mV	< 1 mV	2 mV		Gain Drift ($^\circ\text{C}$)	0.01%	0.01%	0.02%		Max. Input Current (\pm)	100 μA	1 μA	10 nA		DC Input Impedance (\parallel 5 pF)	50 Ω	200 Ω	10 k Ω	Output	Output Voltage	± 10 V (@ > 10 k Ω load)				Output Impedance	50 Ω (terminate with > 10 k Ω load for best performance)				Max. Output Current	± 20 mA				Output Offset	< 1 mV for all gain settings (no signal)				Output Offset Drift	< 20 $\mu\text{V}/^\circ\text{C}$			Digital Control	Control Input Voltage Range	Low: -1 ... +1 V, High: +3 ... +12 V				Control Input Current	0 mA @ 0 V, 1.8 mA @ +5 V, 5 mA @ +12 V		
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<p>Specifications (continued)</p> <p>Power Supply</p> <p>Case</p> <p>Temperature Range</p>	<p>Supply Voltage $\pm 15\text{ V}$</p> <p>Supply Current $+ 60 / - 40\text{ mA typ. (depends on operating conditions, recommended power supply capability min. } \pm 80\text{ mA)}$</p> <p>Weight $0.44\text{ kg (0.97 lbs)}$</p> <p>Material AIMgSi0.5F22, transparent passivated</p> <p>Storage Temperature $-40 \dots +100\text{ }^\circ\text{C}$</p> <p>Operating Temperature $0 \dots +40\text{ }^\circ\text{C}$</p>																													
<p>Absolute Maximum Ratings</p>	<p>Signal Input Voltage $\pm 5\text{ V}$</p> <p>Transient Input Voltage $\pm 3\text{ kV (out of } 200\text{ pF source)}$</p> <p>Control Input Voltage $- 5\text{ V} / + 20\text{ V}$</p> <p>Power Supply Voltage $\pm 22\text{ V}$</p>																													
<p>Connectors</p>	<p>Input $2 \times \text{BNC}$</p> <p>Output $2 \times \text{BNC}$</p> <p>Power Supply and Interface $\text{Sub-D } 15\text{-pin, female, qual. class 2}$</p> <p>Pin 1: $+ 15\text{ V supply voltage}$</p> <p>Pin 2: $- 15\text{ V supply voltage}$</p> <p>Pin 3: $\text{AGND (analog ground)}$</p> <p>Pin 4: not connected</p> <p>Pin 5: $\text{AGND (analog ground)}$</p> <p>Pin 6: not connected</p> <p>Pin 7: $\text{AGND (analog ground)}$</p> <p>Pin 8: not connected</p> <p>Pin 9: $\text{DGND (digital ground for control pins } 10 - 13)$</p> <p>Pin 10: $\text{digital control input: gain channel B, bit B1}$</p> <p>Pin 11: $\text{digital control input: gain channel B, bit B2}$</p> <p>Pin 12: $\text{digital control input: gain channel A, bit A1}$</p> <p>Pin 13: $\text{digital control input: gain channel A, bit A2}$</p> <p>Pin 14: not connected</p> <p>Pin 15: not connected</p>																													
<p>Remote Control Operation</p>	<p>General Remote control input bits are opto-isolated. Select the desired gain setting via a bit code at the corresponding digital inputs.</p> <p>Gain Setting</p> <table border="1" data-bbox="844 1554 1347 1890"> <thead> <tr> <th rowspan="2">Gain (V/A)</th> <th colspan="2">Channel A</th> <th colspan="2">Channel B</th> </tr> <tr> <th>Pin 13 A2</th> <th>Pin 12 A1</th> <th>Pin 11 B2</th> <th>Pin 10 B1</th> </tr> </thead> <tbody> <tr> <td>10^5</td> <td>Low</td> <td>High</td> <td>Low</td> <td>High</td> </tr> <tr> <td>10^7</td> <td>High</td> <td>Low</td> <td>High</td> <td>Low</td> </tr> <tr> <td>10^9</td> <td>Low</td> <td>Low</td> <td>Low</td> <td>Low</td> </tr> <tr> <td>Not defined</td> <td>High</td> <td>High</td> <td>High</td> <td>High</td> </tr> </tbody> </table>	Gain (V/A)	Channel A		Channel B		Pin 13 A2	Pin 12 A1	Pin 11 B2	Pin 10 B1	10^5	Low	High	Low	High	10^7	High	Low	High	Low	10^9	Low	Low	Low	Low	Not defined	High	High	High	High
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Dimensions



All measures in mm unless otherwise noted

02_DLPCA-D-S1_R1

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