

# Pyroelectric Detector Temperature Behavior

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## 1. INTRODUCTION

Detectors that measure radiation by means of the change of temperature of an absorbing material are classified as thermal detectors. Thermal detectors respond to any wavelength radiation that is absorbed, and when an appropriate absorbing material or 'blackening' is applied to the detector element surface, they can be made to respond over a wide range of wavelengths.

Thermal detectors can be classified by their output into a number of categories. One of the most commonly used in infrared (IR) applications, is the pyroelectric detector type that derives its output from changes in the electrical polarization of certain crystals with temperature.

Commercially available single crystal pyroelectric materials have been developed which generate reasonable electric charges for small temperature changes, and at the same time they are stable, uniform and durable. It is this development that has made practical the large scale production and application of cost effective, high performance pyroelectric infrared detectors into a wide variety of commercial, industrial and military applications.

Due to the differing and diverse environmental conditions can adversely affect pyroelectric detector stability and performance, it is extremely important for IR system designers to understand how system requirements can best be translated to optimize detector specifications, and to acquire some basic knowledge of detector characteristics and construction.

As for any IR system, including those using pyroelectric detectors, system designers must determine the magnitude, as well as the spatial characteristics, spectral content and frequency of incoming radiation. An optical system that will deliver this radiation to the detector, must then be designed. Making this basic information available to the detector manufacturer will generate the best suitable detector configuration regarding cost and performance.

Pyroelectric detectors can be used over a wide ambient temperature range without cooling or controlling the detector temperature. As they are typically manufactured from generally available single crystal materials, they are cost effective, and can readily be made into many detector configurations and sizes. In addition, detectors do not require special power supplies and detector signal processing and noise limited operation can be achieved readily with commercially available operational amplifiers and relatively simple circuits.

Pyroelectric detectors will only respond to changes in incoming radiation levels. In addition, because they are constructed from piezoelectric materials, they will all respond to mechanical vibration to some extent although any resulting microphonic noise can be minimized by proper detector design and assembly techniques. Since pyroelectric detectors are often being used to detect small temperature changes, their performance can be affected by external temperature changes such as those caused by air movements. It is, therefore, particularly important for the system designer to understand how these changes affect detector performance.

This application note is offered to provide some insight into how temperature affects the various components that comprise a typical pyroelectric detector and how the effect of ambient temperature variations on detector performance can be minimized. Ambient temperature variations cause **drift** of detector signal and noise, and **fluctuations** or instability in detector output. These two effects should always be considered separately since they are caused by factors that require different corrective measures in detector design.



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## 2. TEMPERATURE DEPENDENT COMPONENTS WITHIN THE PYROELECTRIC DETECTOR

Figure 1 shows the basic commercial single element pyroelectric detector offered by InfraTec GmbH. In the figure there are the temperature dependent active and passive electrical and optical components indicated.

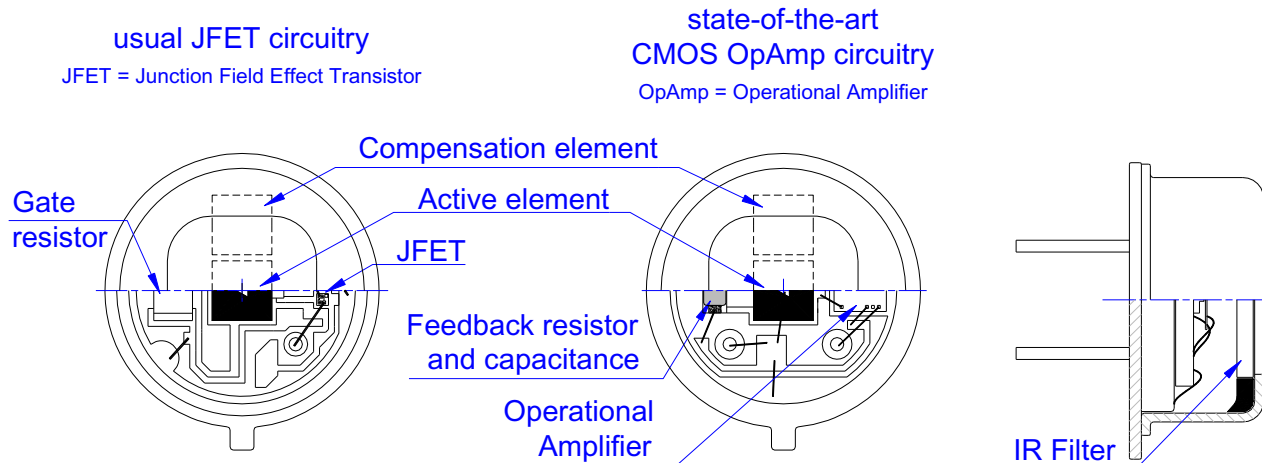


Fig.1 Typical InfraTec Detector Construction

### 2.1. TEMPERATURE DEPENDENCE OF THE PYROELECTRIC CHIP

To varying extents, components labelled in Figure 1 contribute to both **drift** and **fluctuation**, although detector instability due to varying temperatures at fixed operating conditions can generally be attributed mainly to the physical qualities of the detector chip itself.

In a typical application, the active element of the pyroelectric detector is coated with an appropriate 'black' coating to enhance the absorption of chopped or modulated incoming radiation, in the wavelength range of interest defined by an IR filter, thus warming the pyroelectric crystal. Depending on the operating mode of the detector, the resulting current or voltage generated above the capacitance of the detector chip (typically 50pF) is then converted into useful signal. The electrical charges (current) generated even by minimal increases in crystal temperature are on the order of pico- to nanoamperes. It is important to note that an increase in detector case / package temperature, can produce "false" signals.

Electrostatic peaks of up to 140 Volts, for example, have been measured from highly insulated detectors with case temperature increases as small as 10 deg. C.

#### 2.1.1. Temperature Drift / Temperature Coefficient

By definition, the Curie point is that temperature above and beyond which pyroelectric crystals cease to exhibit pyroelectric effects. The pyroelectric coefficient of these materials, which is a measure of eventual detector sensitivity, actually increases with increasing temperature up to the Curie point. In the case of Lithium Tantalate ( $\text{LiTaO}_3$ ), from which almost all of InfraTec's pyroelectric detectors are made, the Curie temperature is +620°C.



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In most practical applications where operating ambient temperatures are substantially lower (<+120°C), the intrinsic temperature coefficient (TC) of the pyroelectric current is approximately +1500ppm/K (+0.15%/K). This is particularly important to know in system applications where the ambient temperature is changing, and this characteristic will manifest itself as detector "drift". Since the compensation element in a thermally compensated detector does not contribute to signal, this TC also applies to compensated detectors.

Standard applications never analyze the pyroelectric current from the active element itself but the signal voltage generated by the pyroelectric current (voltage responsivity) is analyzed. Therefore the resulting TC of the pyroelectric chip depends on the operating mode of the detector either the element in the open-circuit-operation (voltage mode detectors, **JFET circuitry**) or as short circuit (current mode detectors, **OpAmp circuitry**). Only the TC of the pyroelectric current affects if the short circuit mode is applied but the TC will be overlaid by the relative permittivity of the Lithium Tantalate using the open-circuit-operation.

## 2.1.2. Effects of Temperature Fluctuation on Detector Stability

As stated earlier, if not accounted for in the pyroelectric detector design, a change in the detector case temperature alone will usually generate a very low frequency signal, which can be of substantial magnitude. The commonly used Voltage mode detectors with **JFET circuitry** are particularly susceptible to these temperature changes, and signals generated can cause detrimental effects to a chosen preamplifier by pushing the limits of its operational range. The longer the thermal and electrical time constants of these detectors, the more sensitive they are to these changes. The state-of-the-art **CMOS OpAmp** Detectors present in temperature ramps a DC offset adjustment in the same dimension but the wanted signal is 100-times higher. That means that the ratio DC Offset adjustment / Signal will be lower than 1% of the JFET circuitry and hence the dynamics range will not be limited anymore. Since thermal and electrical time constants are determined by detector construction, they can be reduced by design to mitigate, but not completely eliminate, the effects of expected case temperature changes in uncompensated detectors.

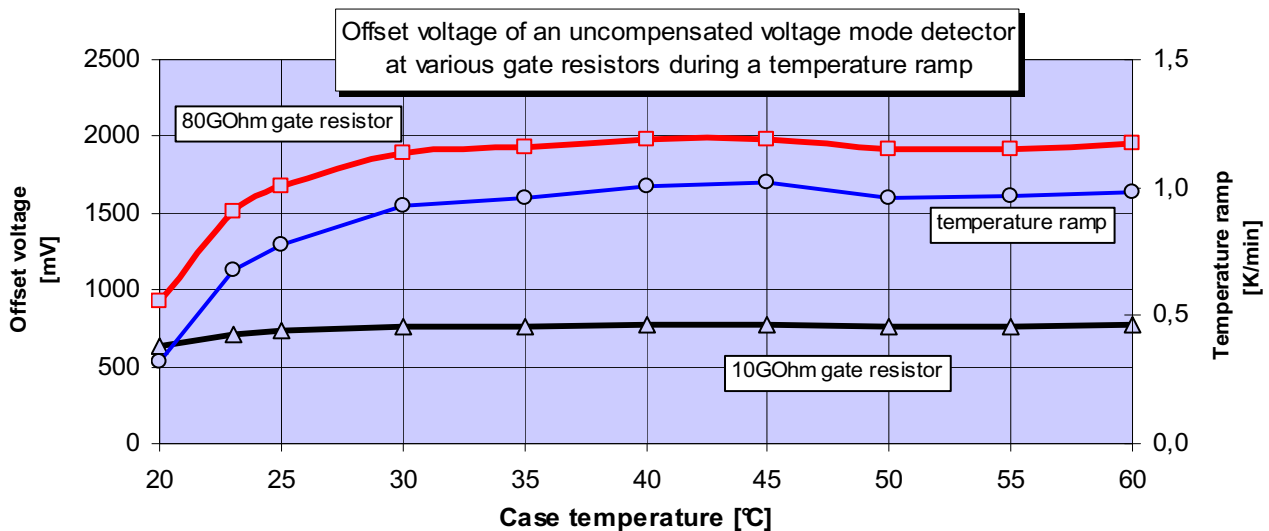


Fig.2 Offset Voltage vs. Temperature at various Gate Resistors (uncompensated voltage mode detectors, JFET circuitry)



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Both can be tailored to meet specific operating conditions although it should be noted that varying the electrical time constant of a detector is not as complex as varying the thermal time constant of a detector. In the InfraTec product line there are only the extended detectors in series LIE-312 and -332 offered with other than standard thermal time constant.

Figure 2 shows the typical effect of temperature variation on detector offset voltage for uncompensated detectors with different gate resistors. Please note that the noise is inversely proportional to the sq. root of the intended resistor decrease, e.g. a 9-times higher stability will cause a decrease of detector detectivity down to 33%.

To overcome the problems associated with detector instability due to case temperature changes, an additional optically inactive detector chip, known as the compensation element, may be added inside the package. The detector is then called a compensated detector. The compensation element will have negative (or the opposite) polarity of that of the active element. As case temperature varies, charges generated at the active and compensation element will essentially have the effect of canceling each other. Figure 3 showing the offset voltage vs. temperature of thermally compensated detectors, clearly presents the effectiveness of this arrangement.

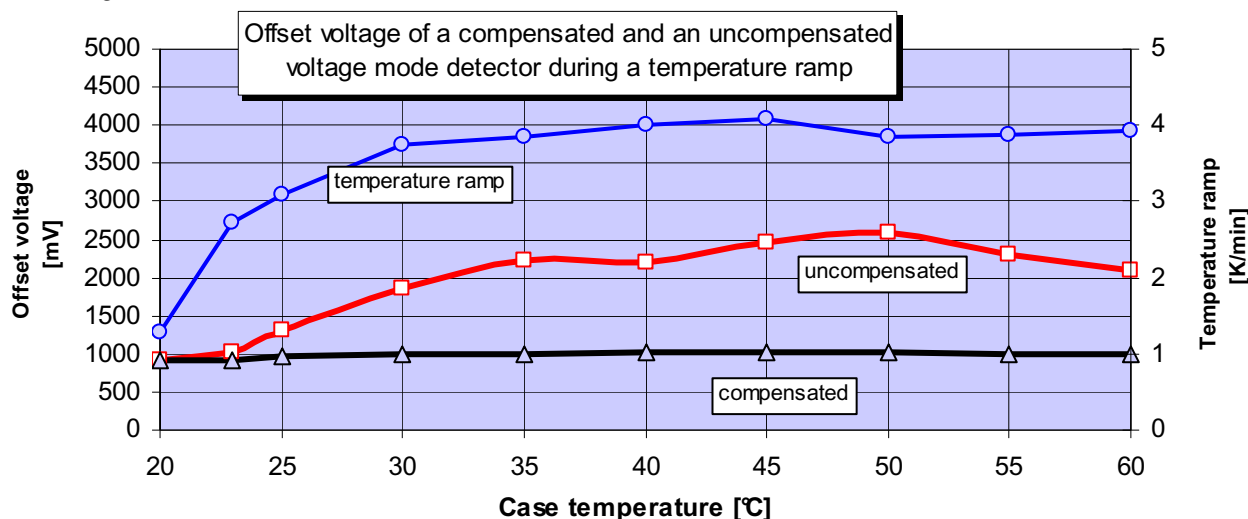


Fig.3 Offset Voltage vs. Temperature (compensated and uncompensated voltage mode detectors, JFET circuitry)

Thermally compensated pyroelectric detectors can be classified into two types: a) serial or b) parallel compensated, depending on the electrical connection of the active and the compensation element (See Fig. 4). The compensation element is completely optically shielded from incoming radiation, hence although optically inactive but still an electrically effective capacitor. The influence of the compensation element regarding signal and detectivity is different between the common **JFET circuitry** and the state-of-the-art **CMOS OpAmp circuitry**.

The net effect of compensation for usual **JFET circuitry** is reduction of detectivity approximately 65 % for both compensation types, although signal and noise will be affected differently for each type. The approximate values of signal and noise for each type of compensated detector, as a percent of those of an uncompensated detector are also shown in the figure above.



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Due to the virtual short of compensation and pyroelectric element in the state-of-the-art **OpAmp circuitry** the parallel compensation neither causes signal nor detectivity losses.

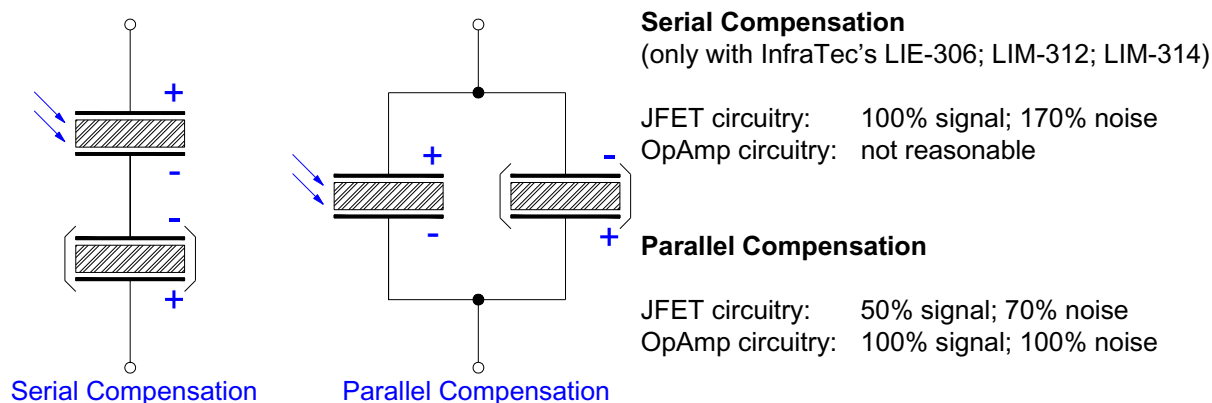


Fig.4 Serial and Parallel Compensation to increase the stability of the DC operating point of pyroelectric detectors in temperature ramps

The decision to use serial or parallel compensation is determined for **JFET** voltage mode detectors by the operating condition from the detector. Parallel-compensated detectors behave more stable at strong (more than 2K/min) or long time constant temperature ramps (longer than 1 hour). On the other hand the user may prefer the double signal of the serial-compensated detector. We always recommend testing parallel-compensation first. Due to the availability of the state-of-the-art **OpAmp detectors** it is no longer necessary to compromise between signal magnitude and stability.

## 2.2. TEMPERATURE DEPENDENCE OF GATE RESISTOR AS WELL AS FEEDBACK COMPONENTS

High value resistors used as Gate resistor in **JFET circuitry** and feedback resistor in **OpAmp circuitry** are characterized by high negative temperature coefficients (i.e. resistance decreases with increasing temperature). 82 and 100 G $\Omega$  resistors that are routinely used with certain types of pyroelectric detectors, for example, typically have **TC**'s of -1500 ... -2000ppm/K.

NPO capacitors as well as printed strip lines are used in the feedback capacitance of the **OpAmp Detectors** and hence there is no measurable temperature influence on the detector parameters.

## 2.3. TEMPERATURE DEPENDENCE OF INTEGRATED JFET AND CMOS OPERATION AMPLIFIER

The gate leakage current, as well as the current noise of integrated **JFETs**, increase substantially with increases in temperature. Particularly at the higher temperatures, this increase is exponential. The gate leakage current a typical FET of less than 1 pA at +25 $^{\circ}$ C, for example, can increase to well over 100 pA at +125 $^{\circ}$ C.

**CMOS operational amplifiers** use a SiO<sub>2</sub> insulated input which enables an ultra low input bias current and low noise. The **OpAmp circuitry** is best suited at high temperatures up to +100  $^{\circ}$ C ( see Fig. 5).



# Pyroelectric Detector Temperature Behavior

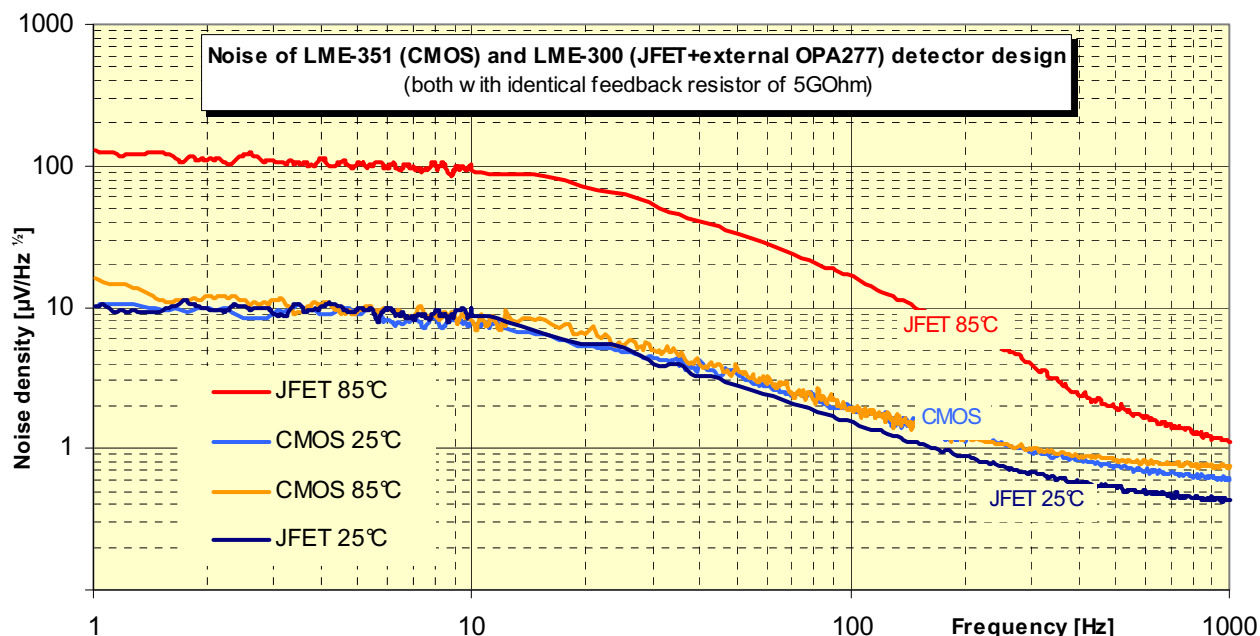


Fig.5 Different behavior of JFET and CMOS input transimpedance amplifiers at temperature increase

Another temperature effect can be created by the amplification drift of **JFET** or **OpAmp**. Increases in temperature will reduce the **JFET** common-source forward transconductance and increase the pinch-off voltage. Due to the high open-loop amplification and strong negative feedback of the **OpAmps** a drift of the **OpAmp circuitry** amplification determined by the temperature is not observed.

## 2.4. TEMPERATURE DEPENDENCE OF IR WINDOWS AND FILTERS

Pyroelectric detectors are coated with a black absorption coating whose spectral characteristics have been shown not to be temperature dependent in the storage and operating range. Please note that the max. storage and operating temperature for detectors with metal black layer (applies only to LIE-312 and LIE-332 used for IR-spectrometer) is only 60°C. Above this temperature the high absorption foamy structure of the metal black layer will irreversibly sinter, which will cause a reduced absorption capability.

To varying degrees, however, the spectral transmission of all windows and optical elements / filters fitted as part of detector package will vary with temperature.

For uncoated IR optical materials (e.g. CaF<sub>2</sub>), temperature variation will mainly affect their transmission and absorption edge (hence their useful range). A signal drift up to -400 ppm arises e.g. for CaF<sub>2</sub> windows.

The spectral thermal shift and changes in transmission characteristics of coated filters, however, is determined by the materials and coating design used to manufacture such filters, and therefore vary by the type of filter (e.g. narrow band pass - NBP, wide band pass - WBP), wavelength region and filter manufacturer.



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Depending on the application InfraTec uses IR filter manufactured with a **low TC design** (typical temperature shift +0.22nm/K, typical angle shift @15° -27nm) or a **low angular shift design** (typical temperature shift +0.40nm/K, typical angle shift @15° -12nm). All integrated narrow bandpass filters in InfraTec pyroelectric detectors for the 3 to 5 micron region have been chosen for their stability in design and are typically deposited on silicon substrates. The temperature shift of a typical filter in this region is shown in Fig. 5 below.

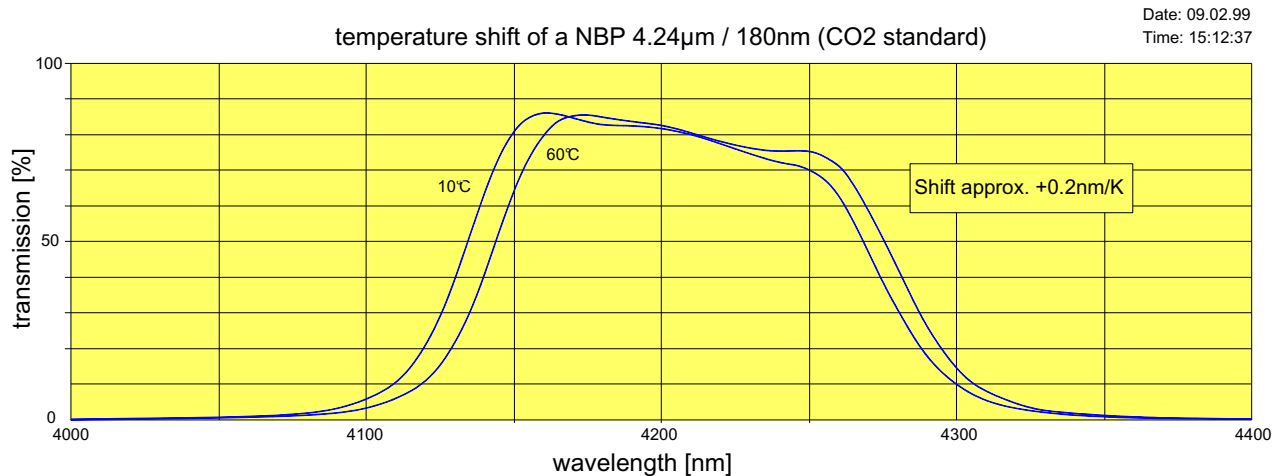


Fig.5 Temperature Shift of 4.24 micron filter (low TC design)

As a general rule, as temperature increases, IR bandpass filters will shift to longer wavelengths with some in band transmission loss. In addition, an angular shift results a CWL shift to shorter wavelengths generally.

### 3. THE EFFECTS OF TEMPERATURE VARIATION ON OVERALL DETECTOR PERFORMANCE

Accounting for the temperature dependence of the key components that constitute a complete detector assembly, some typical data can be offered on detector performance parameters that are more of interest to IR systems designers.

#### 3.1. Detector Offset Voltage ( At a Stabilized Temperature – No Temperature Ramp )

Due to the increase in pinch off voltage and gate leakage current, **higher temperatures will always increase the offset voltage** at the **JFET circuitry** because the negative TC of the gate resistor does not compensate this increase. Figure 6 shows the offset voltage vs. temperature of a typical, but representative, InfraTec detector with **JFET circuitry**.

The DC offset voltage of the **OpAmp circuitry** is determined by the Offset voltage of the **OpAmp** itself (typical in the range of  $\pm 1$ mV) together with input bias current and feedback resistor. The bias current is at 85°C typical +3pA. Figure 7 shows the offset voltage vs. temperature of a typical, but representative, InfraTec detector with **OpAmp circuitry**.



# Pyroelectric Detector Temperature Behavior

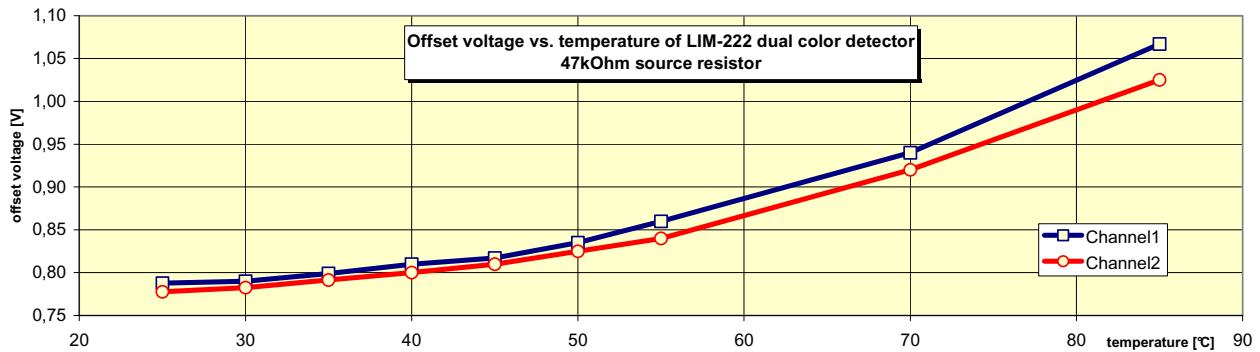


Fig.6 Offset Voltage vs. Temperature (JFET circuitry); after thermal transient period

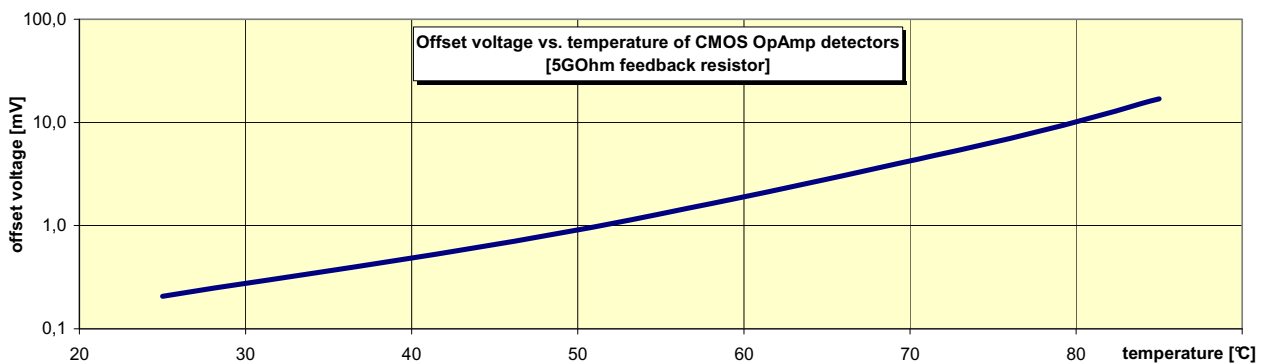


Fig.7 Offset Voltage vs. Temperature (OpAmp circuitry); after thermal transient period

### 3.2. Detector Noise

An increase in gate leakage (**JFET**) or bias (**OpAmp**) current and the decrease in gate (**JFET**) or feedback (**OpAmp**) resistor value with an increase in temperature, results in higher detector noise. This increase in noise is particularly prominent at lower frequencies, see Figure 8 below. As already discussed in 2.3 the noise arising from the **JFET** is considerably greater at higher temperatures compared to the **OpAmp**.

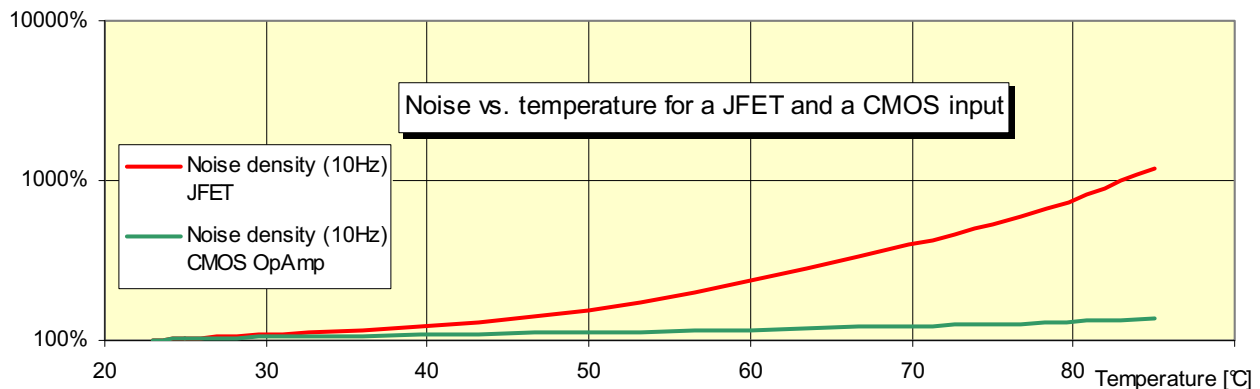


Fig.8 Noise of a JFET and a CMOS OpAmp circuitry at various temperatures



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## 3.3. Signal voltage (responsivity)

InfraTec pyroelectric detector signals are nearly linear with temperature over the most common temperature range for many applications (See Figure 9 for **JFET circuitry**). Please note for **TC** evaluation that a common  $\text{CaF}_2$  window will create an additional **TC** of approx.  $-400\text{ppm/K}$ .

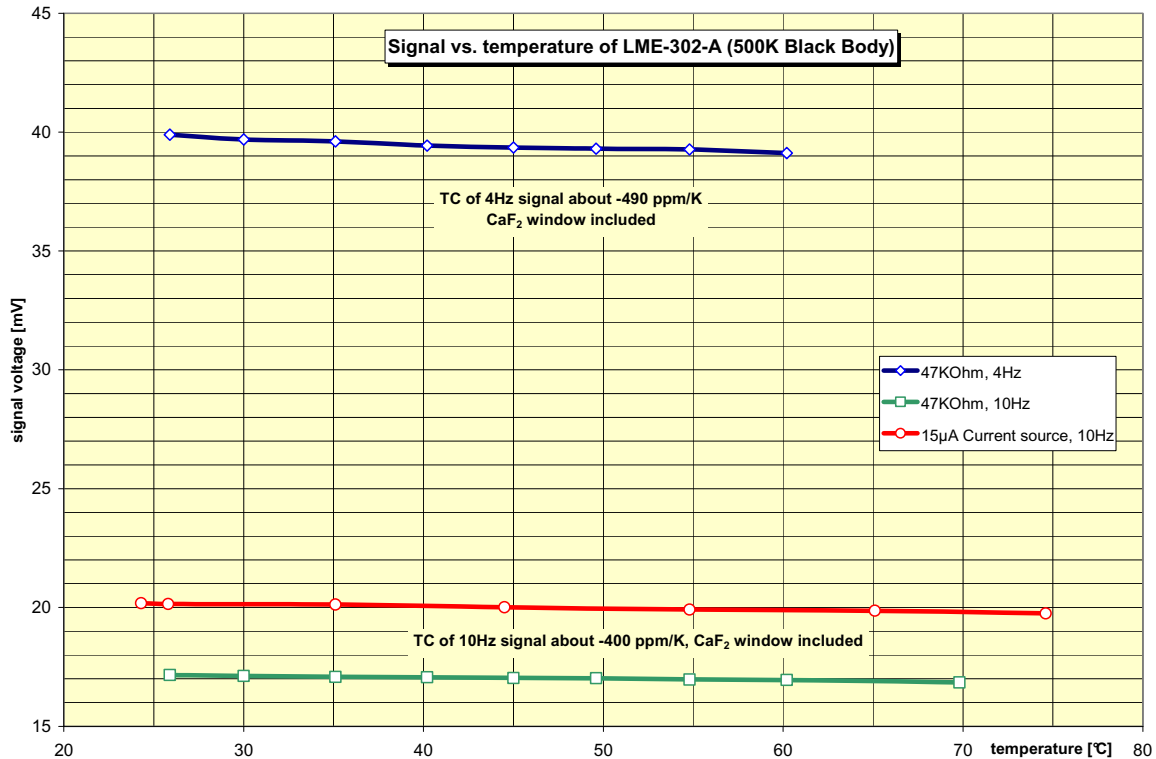


Fig.9 Signal voltage vs. Temperature at JFET circuitry (LME-302-A: voltage mode with  $\text{CaF}_2$  window)

Detectors with **JFET circuitry** without windows have typical **TCs** in the range of  $-200 \dots 200\text{ppm/K}$ , because the **TC** of the single components are almost compensated. A current source instead of the source resistor increases the signal voltage by 10-15% but an influence on the temperature dependency cannot be detected. The negative **TC** of the detector as shown in Fig. 9 is mainly caused by the  $\text{CaF}_2$  window.

The temperature dependency of detectors with **OpAmp circuitry** is more significantly influenced by the modulation frequency. Above the electrical breakpoint the resulting **TC** becomes more and more positive and whereas below the breakpoint more and more negative. The detector LME-351-A has a comparatively high electrical breakpoint of 160Hz due to the feedback resistors of 5GOhm. The shown modulation frequencies in Fig. 10 are smaller and cause without  $\text{CaF}_2$  (**TC**  $-400\text{ppm/K}$ ) to a **TC** of the detectors between  $+300\text{ppm/K}$  (@60Hz) and  $-400\text{ppm/K}$  (@ 4.6Hz). The detector shown in Fig. 11 LIE-245-V has a noticeable greater electrical time constant of 16ms (24GOhm // 0.68pF). The electrical breakpoint is between 4.6Hz and 10Hz and seen as marked limit for the **TC** of the detector.



# Pyroelectric Detector Temperature Behavior

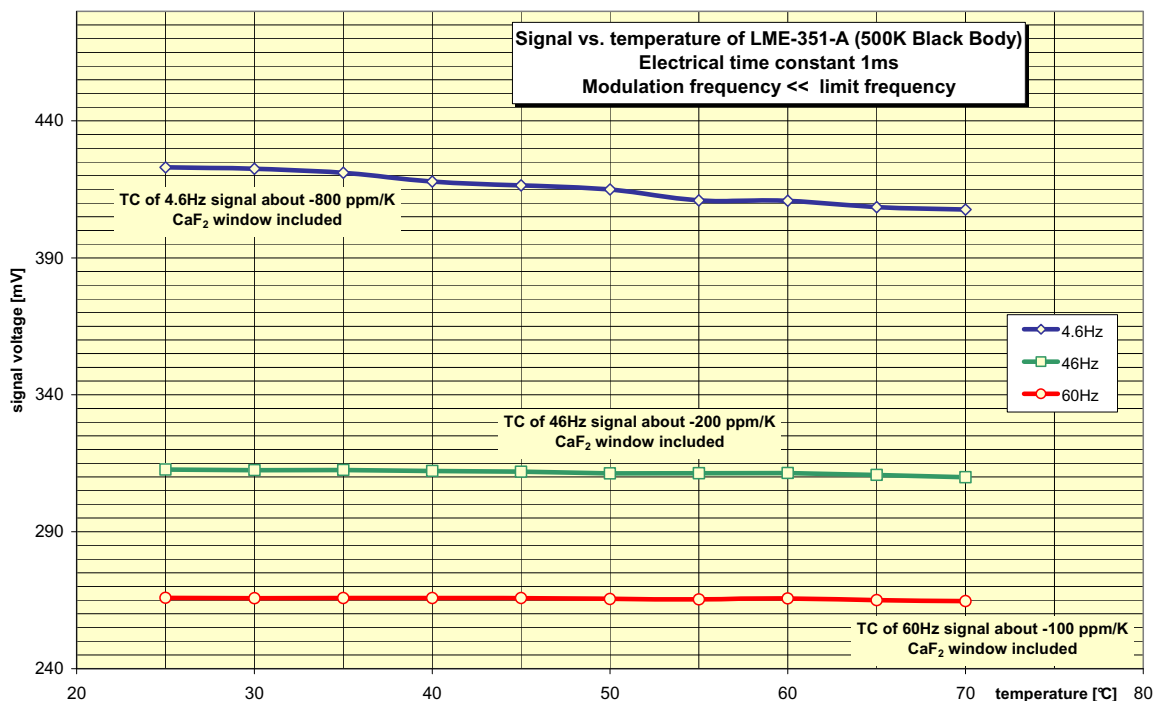


Fig.10 Responsivity vs. Temperature at OpAmp circuitry (LME-351-A: current mode with CaF<sub>2</sub> window)

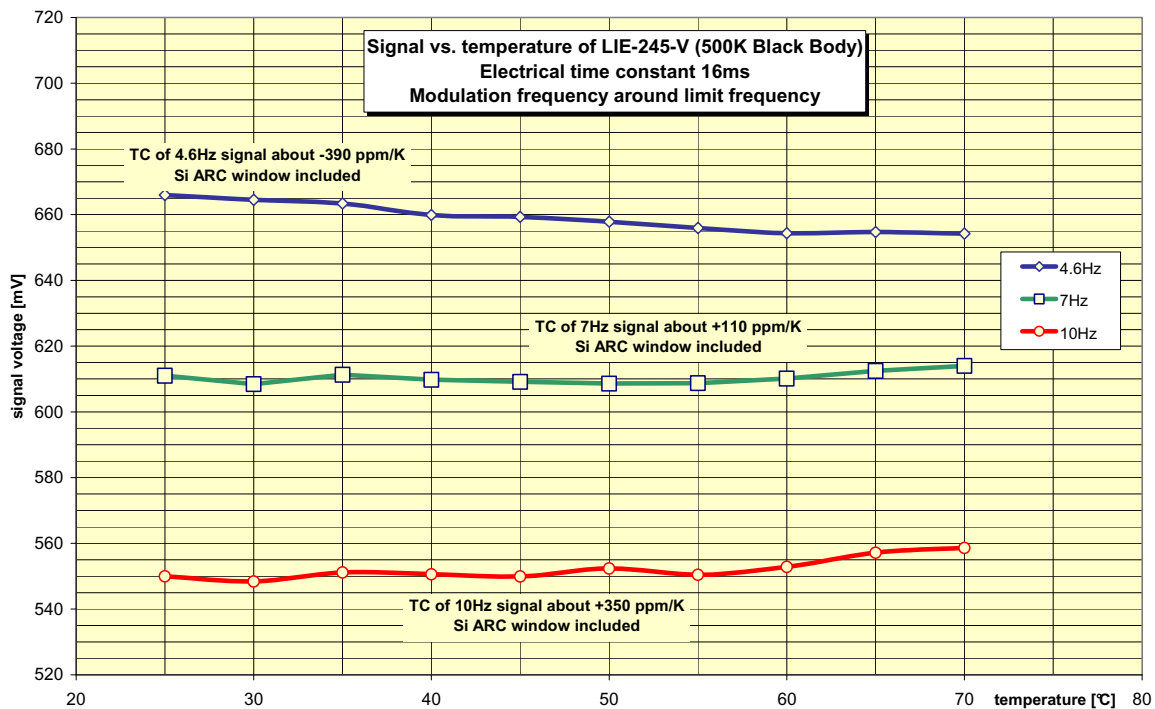


Fig.11 Responsivity vs. Temperature at OpAmp circuitry (LIE-245-V: current mode with Si ARC window)



# Pyroelectric Detector Temperature Behavior

## 4. SUMMARY

With this application note, we have attempted to provide IR system designers with some useful data regarding the effect of ambient temperature variation on pyroelectric detector performance due to the complex interaction of components that form just part of the detector assembly. As pyroelectric detectors are designed and incorporated into a specific application, however, designers must also consider how other system components (external to the detector) such as sources, optics etc. can also alter the expected detector performance.

Data from a simple set-up is included here to demonstrate the importance of understanding how system parameters interact. Figure 12 and 13 below, show responsivity data taken from the same detector being irradiated by two different sources over an ambient temperature range from +25 to +70°C (+85°C). Figure 12 shows the temperature dependence of the signal voltage for both channels of the dual color detector LIM-122-D (**JFET circuitry**) with the commonly used ratio Gas/Reference. A 500K black body source forms the standard illumination considered to be international standard for final test of pyroelectric detectors. However, from experience it is known that a lower modulation frequency causes a poorer TC. The ratio of both channels virtually does not depend upon the temperature.

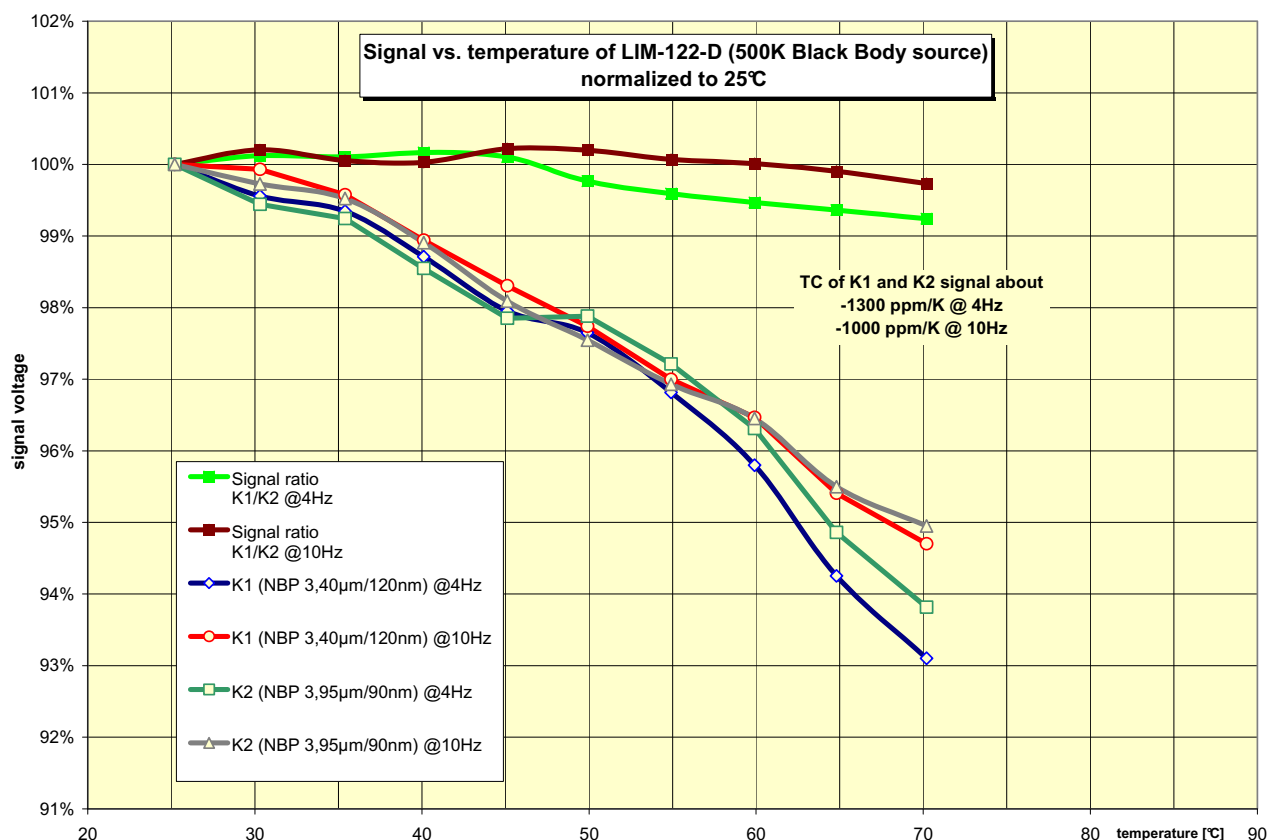


Fig.12 Signal and Signal ratio vs. Temperature of dual color detector (500K Black body)



# Pyroelectric Detector Temperature Behavior

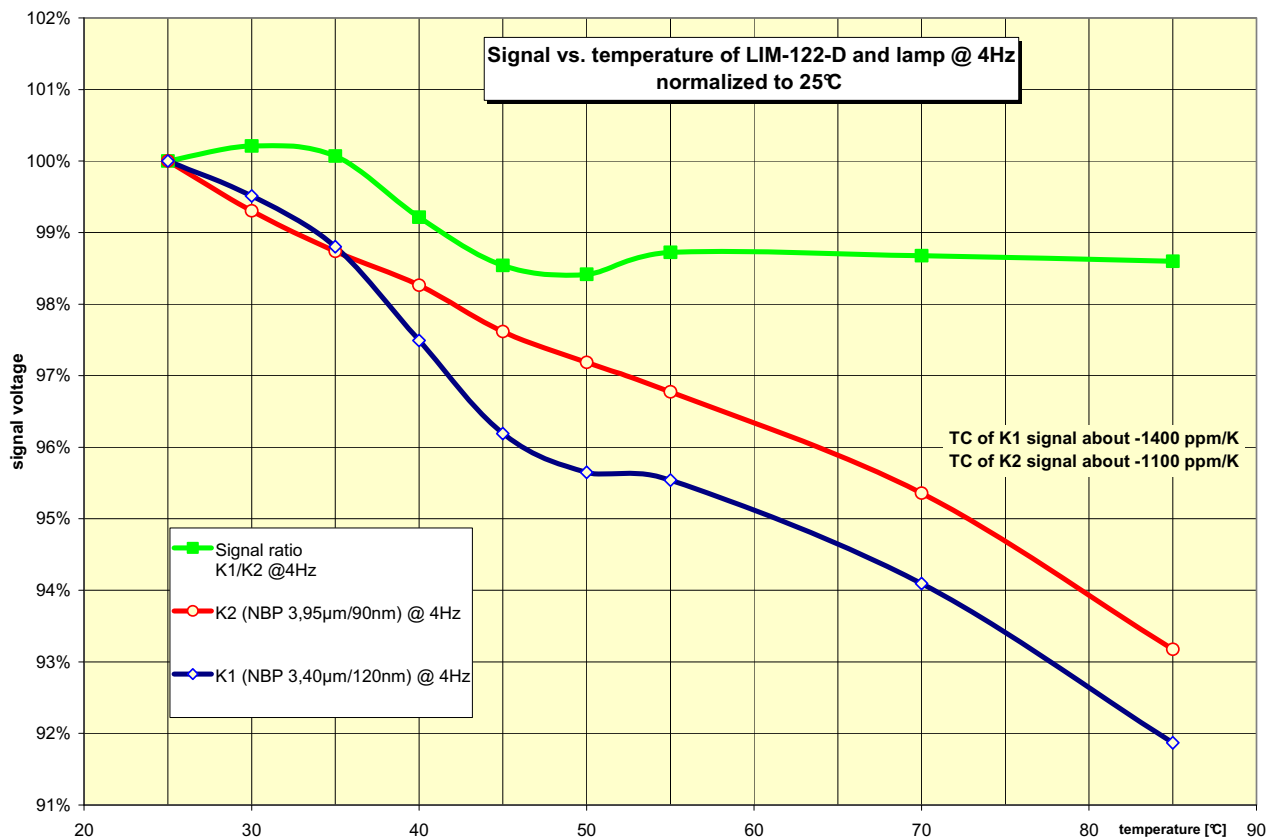


Fig.13 Signal and Signal ratio vs. Temperature of dual color detector (T1 lamp 6V/115mA)

Fig. 13 presents the temperature dependency of an identical dual color detector (**JFET circuitry**) illuminated with a T1-type miniature light bulb 6V/115mA. This thin-walled light bulb is often used in gas analyzers. The modulation frequency is limited to 4Hz by thermal time constant of the lamp filament. Both single channels and signal ratio changes basically with respect to the 500K black body illumination. Besides the lamp glass transmission, additional factors like modulation of the lamp and the positioning accuracy of the optical channel influence the TC and the ratio of the channels.

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