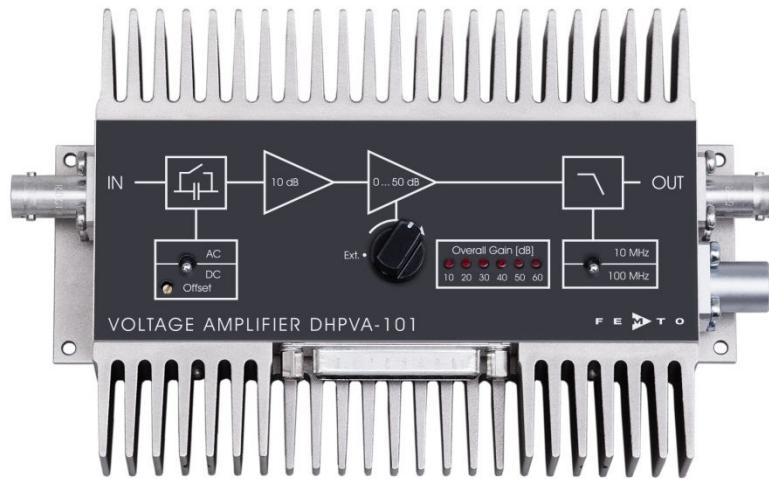


Datasheet
DHPVA-101

Variable Gain 100 MHz Wideband Voltage Amplifier

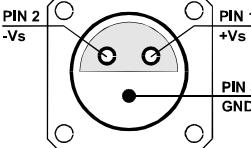


Features	<ul style="list-style-type: none"> Variable gain 10 to 60 dB (x3 to x1,000), switchable in 10 dB steps Bandwidth DC ... 100 MHz, switchable to 10 MHz Built-in temperature compensation for low offset drift of 0.3 μV/$^{\circ}$C 2.3 nV/\sqrtHz input noise Switchable AC/DC-coupling Bandwidth, frequency- and pulse response independent of gain setting Local and remote control DC monitor output
Applications	<ul style="list-style-type: none"> Oscilloscope and transient-recorder preamplifier Photomultiplier and microchannel-plate amplifier Signal-booster for optical receivers and current amplifiers Time-resolved pulse and transient measurements Automated measurement systems Integration in compact systems
Block Diagram	<pre> graph LR INPUT --> AC_DC_Coupling[Programmable AC / DC Coupling] AC_DC_Coupling --> Low_Noise[Low Noise, Low Drift Input Stage] Low_Noise --> Gain10dB[10 dB] Gain10dB --> Five_AMP[5 Programmable Amplifier Stages] Five_AMP --> Lowpass_Filter[Programmable Lowpass-Filter f_3dB = 10/100 MHz] Lowpass_Filter --> OUTPUT AC_DC_Coupling --> MONITOR[Monitor Output Buffer] MONITOR --> MONITOR_OUTPUT[DC MONITOR OUTPUT] PCU[Parameter Control Unit] --- MANUAL_SWITCH[Manual Switch] PCU --- OPTOCOUPLED[Optocoupler Isolate Unit] PCU --- DIGITAL_CONTROL[DIG. CONTROL INPUTS] POWER_SUPPLY[POWER SUPPLY] --- VREG[Supply Voltage Regulator] VREG --- IN15V[IN (+/-15V)] VREG --- OUT12V[OUT (+/-12V, +5V)] VREG --- OUT5V[POWER SUPPLY OUTPUT] </pre> <p>BS01-0540-18</p>

Variable Gain 100 MHz Wideband Voltage Amplifier

Specifications		
Gain	Test conditions	$V_s = \pm 15 V$, $T_A = 25^\circ C$, system impedance = 50Ω
	Gain values	10, 20, 30, 40, 50, 60 dB, switchable
	Gain accuracy	± 0.15 dB
Frequency Response	Lower cut-off frequency	DC / 10 Hz, switchable
	Upper cut-off frequency	100 MHz, switchable to 10 MHz (approx. Bessel filter characteristic for clean pulse response)
	Upper cut-off frequency roll-off	see frequency response diagram (page 4)
Time Response	Rise/fall time (10 % - 90 %)	3.5 ns (@ 100 MHz) 35 ns (@ 10 MHz)
Input	Input impedance	50Ω
	Input return loss S11	-37 dB @ 50 MHz -31 dB @ 100 MHz -21 dB @ 200 MHz
	Input voltage drift	0.3 $\mu V/^{\circ}C$
	Equivalent input voltage noise	2.3 nV/ \sqrt{Hz} (@ 30 - 60 dB gain)
	Equivalent input current noise	3.0 pA/ \sqrt{Hz}
	1/f-noise corner	20 kHz
	Input bias current	<200 nA
	Input offset voltage	-10 mV ... +10 mV, adjustable by offset-potentiometer and external control voltage
Output	Output impedance	50Ω (terminate with 50Ω load for best performance)
	Output return loss S22	-40 dB @ 50 MHz -35 dB @ 100 MHz -31 dB @ 200 MHz
	Output voltage range	$2 V_{pp}$ (for linear amplification)
	Output power (max.)	+10 dBm
	Output current (max.)	70 mA
	THD	<0.5 % (@ 10 MHz, 1 V_{pp})
Monitor Output	Monitor output gain	x1 (@ $\geq 1 M\Omega$ load)
	Monitor output voltage range	$\pm 5 V$
	Monitor output current	± 10 mA
	Monitor output bandwidth	DC ... 100 kHz
	Monitor output impedance	50Ω (designed for $\geq 1 M\Omega$ load)
Indicator LED	Function	gain setting
Digital Control	Control input voltage range	Low: -0.8 ... +0.8 V High: +1.8 ... +12 V, TTL / CMOS compatible
	Control input current	0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V
	Gain control switching time	5 ms
Ext. Offset Control	Control voltage range	± 10 V, corresponds to ± 10 mV input offset
	Offset control input impedance	$20 k\Omega$
Power Supply	Supply voltage	± 15 V
	Supply current	± 120 mA typ. (depends on operating conditions) ± 400 mA max.
	Stabilized power supply output	± 12 V / max. 50 mA, +5V / max. 50 mA

Variable Gain 100 MHz Wideband Voltage Amplifier

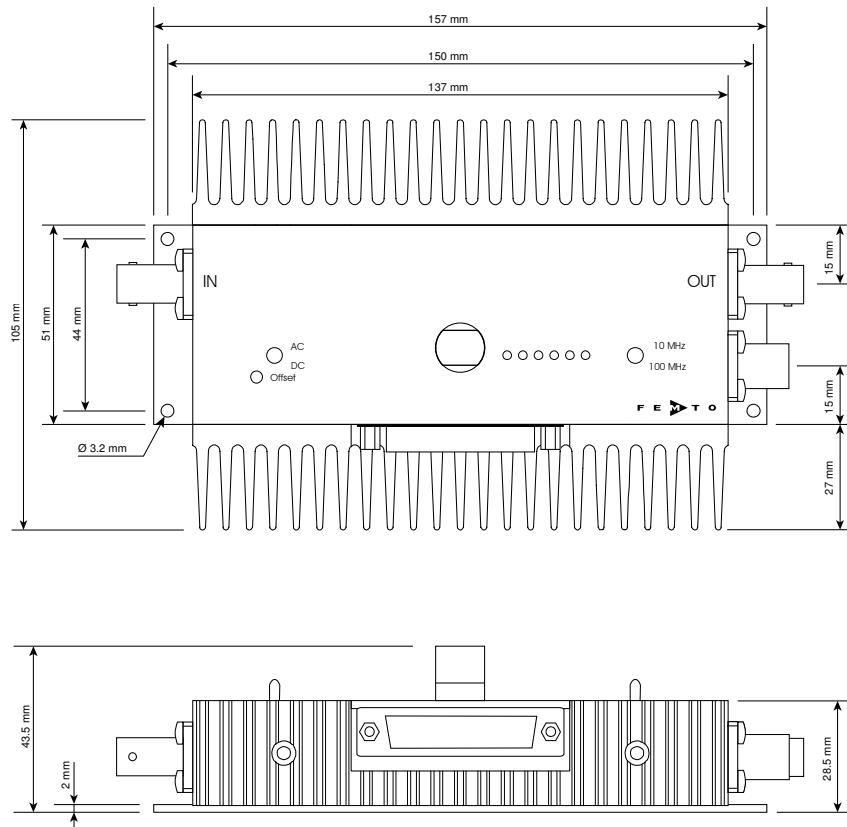
Specifications (continued)		
Case	Weight Material	560 g (1.24 lb) AlMg4.5Mn, nickel-plated
Temperature Range	Storage temperature Operating temperature	−40 °C ... +85 °C 0 °C ... +50 °C
Absolute Maximum Ratings	Power supply voltage Signal input voltage Digital control input voltage	±16.5 V ±5 V +16 V / −5 V
Connectors	Input Output Power supply Control port	BNC jack (female) BNC jack (female) Lemo® series 1S, 3-pin fixed socket (mating plug type: FFA.1S.303.CLAC52) Pin 1: +15 V Pin 2: −15 V Pin 3: GND  Sub-D 25-pin, female, qual. class 2 Pin 1: +12 V (stabilized power supply output) Pin 2: −12 V (stabilized power supply output) Pin 3: AGND (analog ground) Pin 4: +5 V (stabilized power supply output) Pin 5: monitor output Pin 6, 7: NC Pin 8: offset control voltage input Pin 9: DGND (ground f. digital control pin 10 - 25) Pin 10: digital control input: gain, LSB Pin 11: digital control input: gain Pin 12: digital control input: gain, MSB Pin 13: digital control input: AC/DC Pin 14: digital control input: 100 MHz/10 MHz Pin 15 - 25: NC

Variable Gain 100 MHz Wideband Voltage Amplifier

Remote Control Operation	General	Remote control input bits are opto-isolated and connected by logical OR to local switch setting. For remote control a switch setting, set the corresponding local switch to "Ext.", "AC" or "10 MHz" and select the wanted setting via a bit-code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote controlled bandwidth setting is also possible.			
	Gain setting	Gain	Pin 10	Pin 11	Pin 12
		10 dB	low	low	low
		20 dB	high	low	low
Typical Performance Characteristics	AC/DC setting	Coupling	Pin 13		
		AC	low		
		DC	high		
	Bandwidth setting	Bandwidth	Pin 14		
		10 MHz	low		
		100 MHz	high		
Frequency response (logarithmic)					
<p>The graph illustrates the frequency response of the DHPVA-101 amplifier across a wide frequency range. The Y-axis represents the signal level in dB, ranging from -50 to 70. The X-axis represents frequency on a logarithmic scale, with major ticks at 1 kHz, 10 kHz, 100 kHz, 1 MHz, 10 MHz, 100 MHz, and 1000 MHz. Multiple colored curves are plotted, each representing a different gain setting. All curves show a peak around 60 dB at approximately 100 kHz, followed by a roll-off. The higher the gain, the higher the frequency at which the response drops to -3dB.</p>					

Variable Gain 100 MHz Wideband Voltage Amplifier

Dimensions



One or both heat sinks may be removed (two recessed head screws) if sufficient cooling of the case is provided otherwise (< 2 K/W), for example by mounting the amplifier with good thermal contact on a sufficiently large solid metal case/rack system.