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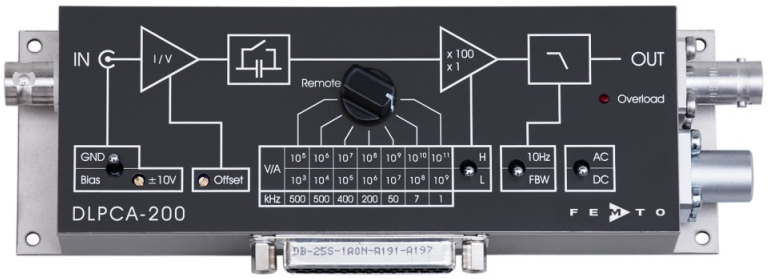
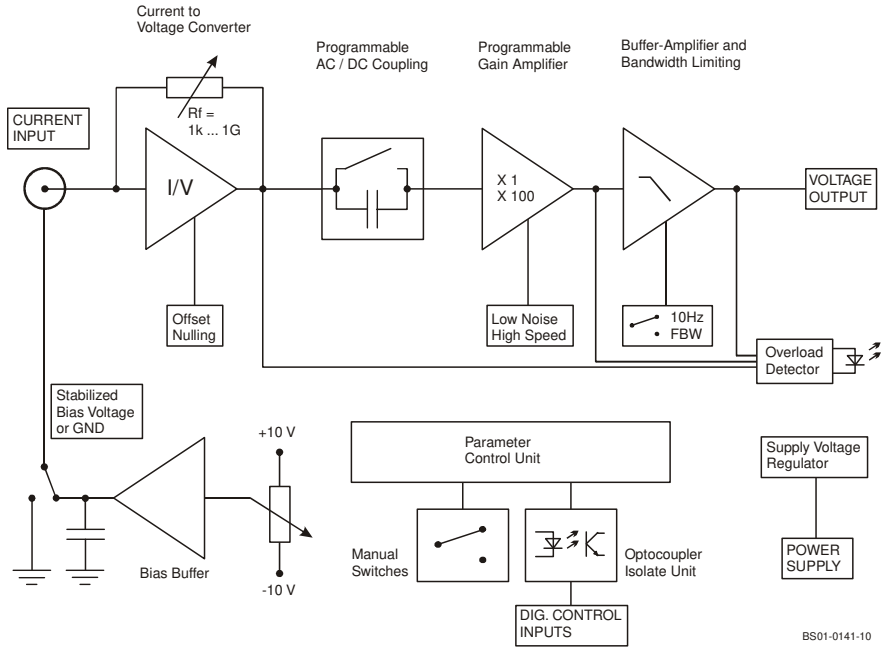
Datasheet DLPCA-200

Variable Gain
Low Noise Current Amplifier



Features	• **Transimpedance (gain) switchable from 1 x 10^3 to 1 x 10^{11} V/A** • **Bandwidth DC/1 Hz ... 500 kHz** • **Bandwidth switchable to DC ... 10 Hz for low noise DC measurements** • **Bandwidth independent of detector capacitance (up to 1 nF)** • **Adjustable bias voltage** • **Protection against ±3 kV transients** • **Local and remote control**
Applications	• **Photodiode and photomultiplier amplifier** • **Scanning tunneling microscopy (STM)** • **Spectroscopy** • **Beam monitoring for particle accelerators/synchrotrons** • **Ionisation detectors** • **Preamplifier for lock-ins, A/D converters, etc.**
Block Diagram	

Variable Gain
Low Noise Current Amplifier

Specifications

Test conditions: $V_S = \pm 15$ V, $T_A = 25$ °C, load impedance = 1 MΩ

Gain

Transimpedance	1×10^3 ... 1×10^{11} V/A
Gain accuracy	±1 %
Gain drift	see table below

Frequency Response

Lower cut-off frequency	DC / 1 Hz
Upper cut-off frequency (–3 dB)	up to 500 kHz (see table below), switchable to 10 Hz
Gain flatness	±0.1 dB

Input

Equ. input noise current	see table below
Equ. input noise voltage	4 nV/√Hz (@ 1 kHz)
Input offset current drift	see table below
Input bias current	1 pA typ. (max. 3 pA)
Max. input current	see table below (value for linear amplification)
Input offset compensation	adjustable by offset potentiometer and external control voltage; max. range see table below

Performance depending on Gain Setting

Gain setting (low noise) (V/A)	10^3	10^4	10^5	10^6	10^7	10^8	10^9
Upper cut-off frequency (–3 dB)	500 kHz	500 kHz	400 kHz	200 kHz	50 kHz	7 kHz	1.1 kHz
Rise/fall time (10 % - 90 %)	700 ns	700 ns	900 ns	1.8 µs	7 µs	50 µs	300 µs
Input noise current density (/√Hz)	20 pA	2.3 pA	450 fA	130 fA	43 fA	13 fA	4.3 fA
measured at	10 kHz	10 kHz	10 kHz	1 kHz	1 kHz	100 Hz	100 Hz
Integr. input noise current (rms)*	21 nA	2.4 nA	500 pA	130 pA	41 pA	5.8 pA	0.8 pA
Offset current drift (/°C)	30 nA	3 nA	0.3 nA	30 pA	3 pA	0.3 pA	0.1 pA
Gain drift (/°C)	0.008%	0.008%	0.008%	0.01%	0.01%	0.01%	0.02%
Max. input current (±)	10 mA	1 mA	0.1 mA	10 µA	1 µA	0.1 µA	10 nA
Input offset compensation (±)	100 µA	10 µA	1 µA	0.1 µA	10 nA	1 nA	0.1 nA
DC input impedance (// 5 pF)	50 Ω	50 Ω	50 Ω	60 Ω	150 Ω	1 kΩ	10 kΩ

Gain setting (high speed) (V/A)	10^5	10^6	10^7	10^8	10^9	10^{10}	10^{11}
Upper cut-off frequency (–3 dB)	500 kHz	500 kHz	400 kHz	200 kHz	50 kHz	7 kHz	1.1 kHz
Rise/fall time (10 % - 90 %)	700 ns	700 ns	900 ns	1.8 µs	7 µs	50 µs	300 µs
Input noise current density (/√Hz)	13 pA	1.8 pA	440 fA	130 fA	43 fA	13 fA	4.3 fA
measured at	10 kHz	10 kHz	10 kHz	1 kHz	1 kHz	100 Hz	100 Hz
Integr. input noise current (rms)*	12 nA	1.8 nA	450 pA	120 pA	37 pA	5.3 pA	0.8 pA
Offset current drift (/°C)	30 nA	3 nA	0.3 nA	30 pA	3 pA	0.3 pA	0.1 pA
Gain drift (/°C)	0.008%	0.008%	0.008%	0.01%	0.01%	0.01%	0.02%
Max. input current (±)	100 µA	10 µA	1 µA	0.1 µA	10 nA	1 nA	0.1 nA
Input offset compensation (±)	100 µA	10 µA	1 µA	0.1 µA	10 nA	1 nA	0.1 nA
DC input impedance (// 5 pF)	50 Ω	50 Ω	50 Ω	60 Ω	150 Ω	1 kΩ	10 kΩ

* The integrated input noise is measured with an open but shielded amplifier input in the full bandwidth ("FBW") setting. The input referred peak-peak noise can be calculated from the rms noise as follows: $I_{PP} = I_{RMS} \times 6$

The output noise is given by: $U_{PP} = I_{PP} \times \text{gain}$

Output

Output voltage	±10 V (@ ≥100 kΩ load)
Output impedance	50 Ω (terminate with ≥100 kΩ load for best performance)
Max. output current	±30 mA

Detector Bias

Bias voltage range	±10 V, max. 22 mA (bias voltage connected to shield of BNC input socket, adjustable by potentiometer, switchable to GND)

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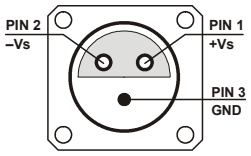
Indicator LED	Function	overload
Digital Control	Control input voltage range	LOW bit: –0.8 V ... +1.2 V, HIGH bit: 2.3 V ... +12 V
	Control input current	0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V
	Overload output	non active: <0.4 V @ 0 ... –1 mA active: typ. 5 ... 5.1 V @ 0 ... 2 mA
Ext. Offset Control	Control voltage range	±10 V
	Offset control input impedance	20 kΩ
Power Supply	Supply voltage	±15 V
	Supply current	+120 / –80 mA typ. (depends on operating conditions, recommended power supply capability min. ±200 mA)
	Stabilized power supply output	±12 V, max. ±50 mA, +5V, max. 30 mA
Case	Weight	320 g (0.74 lb.)
	Material	AlMg4.5Mn, nickel-plated
Temperature Range	Storage temperature	–40 °C ... +100 °C
	Operating temperature	0 °C ... +60 °C
Absolute Maximum Ratings	Signal input voltage	–16 V / +12 V
	Signal input current (rms)	35 mA
	Transient input voltage	±3 kV (out of 200 pF source)
	Control input voltage	–5 V / +16 V
	Power supply voltage	±20 V
Connectors	Input	BNC, isolated, jack (female)
	Output	BNC, jack (female)
	Detector bias output	shield of input BNC
	Power supply	Lemo® series 1S, 3-pin fixed socket (mating plug type: FFA.1S.303.CLAC52) Pin 1: +15V Pin 2: –15V Pin 3: GND
	Control port	Sub-D 25-pin, female, qual. class 2 Pin 1: +12 V (stabilized power supply output) Pin 2: –12 V (stabilized power supply output) Pin 3: AGND (analog ground) Pin 4: +5 V (stabilized power supply output) Pin 5: digital output: overload (referred to pin 3) Pin 6: signal output (connected to BNC) Pin 7: NC Pin 8: input offset control voltage Pin 9: DGND (ground for digital control pins 10 - 14) Pin 10: digital control input: gain, LSB Pin 11: digital control input: gain Pin 12: digital control input: gain, MSB Pin 13: digital control input: AC/DC Pin 14: digital control input: high speed / low noise Pin 15 - 25: NC



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Remote Control Operation

General

Remote control input bits are opto-isolated and connected by logical OR function to local switch settings. For remote control set the corresponding local switches to “Remote”, “AC” and “H” (High speed) and select the wanted setting via a bit code at the corresponding digital inputs.
Mixed operation, e.g. local gain setting and remote controlled AC/DC setting, is also possible.

Switch settings “FBW / 10 Hz” and “Bias / GND” are not remote controllable.

Gain setting

Low noise Pin 14=HIGH Gain (V/A)	High speed Pin 14=LOW Gain (V/A)	Pin 12 MSB	Pin 11	Pin 10 LSB
10^3	10^5	LOW	LOW	LOW
10^4	10^6	LOW	LOW	HIGH
10^5	10^7	LOW	HIGH	LOW
10^6	10^8	LOW	HIGH	HIGH
10^7	10^9	HIGH	LOW	LOW
10^8	10^{10}	HIGH	LOW	HIGH
10^9	10^{11}	HIGH	HIGH	LOW

Gain settling time

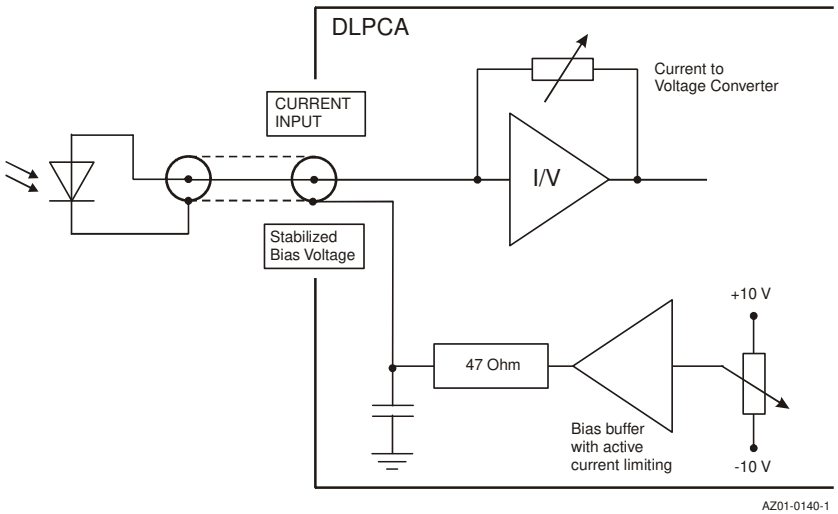
<150 ms

AC/DC setting

Coupling	Pin 13
AC	LOW
DC	HIGH

Application Diagram

Photo detector biasing



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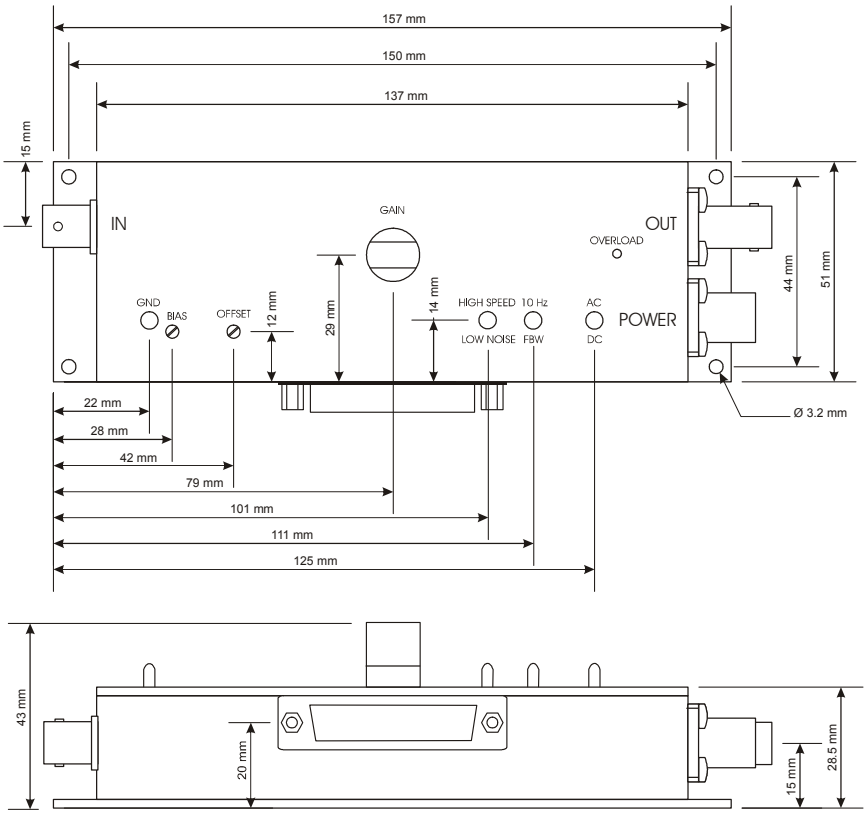
Dimensions



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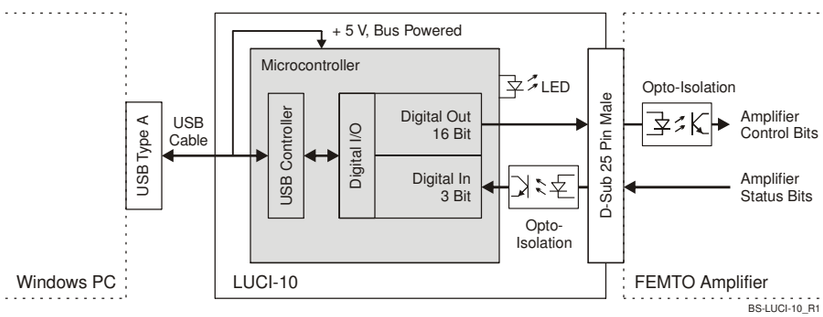
Datasheet LUCI-10

USB to D-Sub Control Interface for FEMTO Amplifiers

Features	• **Compact digital I/O interface for USB remote control of FEMTO amplifiers** • **Supports opto-isolation of amplifier signal path from PC USB port** • **16 digital outputs, 3 opto-isolated digital inputs** • **Bus-powered operation** • **System driver, application software and VI's for use with LabVIEW™ included**
Applications	• **Remote control of FEMTO® amplifiers and photoreceivers directly from a PC**
Block Diagram	

Hardware Specifications

General Characteristics	Bus interface	USB 2.0 (full-speed)
	Digital I/O channels	16 output lines 3 opto-isolated input lines
	Supply	PC USB port, +5 V, typ. 100 mA, bus-powered (no auxiliary power supply required)
	Connectors	USB type A D-Sub, 25 pin, male
	Cable	AWG 28, length 1.8 m
Output	Number of channels	16 output lines, supporting opto-isolation inside FEMTO amplifiers and photoreceivers
	Output voltage range	LOW bit: 0 … +0.5 V (@ 0 … 2 mA output current) HIGH bit: +4 … +5.5 V (@ 0 … 2 mA output current)
	Max. current	6 mA per channel
	Writing rate	max. 600 operations per second

USB to D-Sub Control Interface for FEMTO Amplifiers

Input	Number of channels	3 opto-isolated input lines
	Input voltage range	LOW bit: –20 … +1.5 V HIGH bit: +3 … +20 V
	Switching current	1 mA typ. @ 5 V
	Reading rate	max. 300 operations per second
Power Supply	USB port, bus powered	+4.5 … +5.5 V DC
	Active current	max. 200 mA / typ. 100 mA
	Suspend current	<0.5 mA (standby mode of Windows®)
Case	D-Sub case	metal hood (EMI/RFI shielding), with jack screws
	Weight	130 g (0.3 lb.)
	Material	zinc die-cast, nickel plated
Temperature Range	Storage temperature	–40 … +100 °C
	Operating temperature	0 … +50 °C
Absolute Maximum Ratings	Max. voltage at input	±30 V
	Max. short-circuit output current	±20 mA per channel, 200 mA total
	Max. isolation voltage	±60 V (input ground to output ground)
Connectors	Device port	D-Sub, 25 pin, male Pin 1: NC Pin 2: NC Pin 3: GND (IN) Pin 4: NC Pin 5: Digital IN Pin 6: Digital IN Pin 7: Digital IN Pin 8: NC Pin 9: GND (OUT) Pin 10: Digital OUT Low Byte, LSB Pin 11: Digital OUT Low Byte Pin 12: Digital OUT Low Byte Pin 13: Digital OUT Low Byte Pin 14: Digital OUT Low Byte Pin 15: Digital OUT Low Byte Pin 16: Digital OUT Low Byte Pin 17: Digital OUT Low Byte, MSB Pin 18: Digital OUT High Byte, LSB Pin 19: Digital OUT High Byte Pin 20: Digital OUT High Byte Pin 21: Digital OUT High Byte Pin 22: Digital OUT High Byte Pin 23: Digital OUT High Byte Pin 24: Digital OUT High Byte Pin 25: Digital OUT High Byte, MSB
	PC port	USB type A

USB to D-Sub Control Interface for FEMTO Amplifiers

Software Specifications

Software (included on CD)

Device driver	dynamic link library (DLL) for integration in Microsoft Windows® 32 bit & 64 bit operating system for use with C/C++, LabWindows™ /CVI™ or LabVIEW™
Application software	GUI (graphical user interface) programs for simple remote control of FEMTO amplifiers and photoreceivers provided as executable programs and LabVIEW projects
LabVIEW programs	sample programs to control and test the LUCI-10 hardware (including front panel and block diagram)
LabVIEW library	special VI toolkit for integration in LabVIEW 32 bit & 64 bit development environment

Note: A National Instruments LabVIEW™ license is not included in this software package. For use of the GUI application programs the LabVIEW Run-Time Engine is required. If not detected on the host PC during the installation process the LabVIEW Run-Time Engine will be installed automatically from the CD.

System Requirements

Operating system	Microsoft Windows XP with Service Pack 3, or higher
Processor	Intel Pentium III or AMD Athlon, or better
System memory	1 GB of RAM, or more
Hard disk space	about 5 GB
Interface port	USB 1.1 or USB 2.0
Supported FEMTO modules	any standard FEMTO amplifier or photoreceiver with 25 pin D-Sub socket, except model HLVA-100

Optional Requirements

For development of own application programs an additional development environment like LabVIEW Version 2012 (or higher) or C/C++ is required.

Legal Notice

USB to D-Sub Control Interface for FEMTO Amplifiers

Dimensions