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### **Datasheet**

### **DLPVA-100-BLN-S**

# Low-Noise Variable Gain Low-Frequency Voltage Amplifier



| Features      | <ul> <li>Variable gain 40 to 100 dB, switchable in 20 dB steps</li> <li>Bipolar input stage, recommended for low impedance sources smaller than 100 Ω</li> <li>Very low input voltage noise: 700 pV/√Hz</li> <li>DC-coupled, single ended</li> <li>DC-drift 0.5 µV/°C</li> <li>Bandwidth DC - 100 kHz, switchable to 1 kHz</li> <li>Switchable AC/DC-coupling</li> <li>Local and remote control</li> </ul>   |
|---------------|--|
| Applications  | <ul> <li>Low-noise laboratory amplifier</li> <li>Pulsed thermal EMF analysis</li> <li>Industrial sensors</li> <li>Detector preamplifier</li> <li>Integrated measurement systems</li> </ul>   |
| Block Diagram | Low Noise, Low Drift Input Stage Programmable Lowpass-Filter AC / DC Coupling Gain Amplifier Gain Amplifier Lowpass-Filter  40 dB  Programmable AC / DC Coupling Gain Amplifier Gain Amplifier Lowpass-Filter  O dB  O d |

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

OFFSET ADJUST INPUT / TRIMMER

F E T O

POWER SUPPLY OUTPUT

DIG. CONTROL INPUTS

### Low-Noise Variable Gain **Low-Frequency Voltage Amplifier**

 $V_s = \pm 15 \text{ V}, T_A = 25 \text{ °C}, \text{ load impedance} = 1 \text{ M}\Omega$ Specifications Test conditions

40, 60, 80, 100 dB Gain Gain values

Indicated by four LEDs

±0.1 % (between settings) Gain accuracy

±1 % (overall)

Gain flatness ±0.1 dB

Frequency Response

Lower cut-off frequency DC, switchable to 1.5 Hz Upper cut-off frequency 100 kHz, switchable to 1 kHz

Upper cut-off frequency rolloff 12 dB/oct.

Time Response Rise/fall time (10 % - 90 %)  $3.5 \mu s$  (@ BW = 100 kHz)

 $350 \mu s (@ BW = 1 kHz)$ 

Input Input impedance  $1 M\Omega$ Input capacitance 13 pF

0.5 µV/°C Input voltage drift

Equivalent input voltage noise Gain setting noise 700 pV/√Hz (100 Hz ... 100 kHz) 100 dB

> 80 dB 730 pV/√Hz 860 pV/√Hz 60 dB 6 nV/√Hz 40 dB

3 pA/√Hz Equivalent input current noise 1/f-noise corner 80 Hz Input Bias current 1 μΑ 8 nA/°C Input bias current drift

Input offset voltage ±500 µV, adjustable by offset trimmer and external

control voltage

Output Output impedance <100  $\Omega$  (terminate with > 10 k $\Omega$  load for best

performance)

Output voltage range

For linear amplification  $\pm 10 \text{ V } (@ > 10 \text{ k}Ω \text{ load})$ 

Output current (max.) ±20 mA

Output overload recovery time 0.5 ms (after 20 x overload)

Overload LED The amplifier features a LED to indicate an overload condition. The Overload LED will turn on

if the signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until

the Overload LED turns off.

The Overload LED may also turn on when the amplifier is operated with open input or with a high source resistance, e. g. external AC coupling. In this case the bias current may cause a considerable input voltage. For proper operation please use a source resistance of less than

1 k $\Omega$  or switch to a lower gain setting.

Remote Offset Control Offset control voltage range  $\pm 10$  V, corresponds to  $\pm 500~\mu V$  input offset voltage

> Offset control input impedance  $200 \text{ k}\Omega$

Remote Digital Control Control input voltage range Low: -0.8 ...+0.8 V

Control input current

High: +1.8 ... +15 V, TTL / CMOS compatible 0 mA @ 0 V, 1.5 mA @ +5 V, 4.0 mA @ +12 V

Overload output Non active: +5 V, max. 1 mA, active: 0.8 V, max. -10 mA;

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Specifications (continued)

Power Supply Supply voltage  $\pm 15 \text{ V} (\pm 14.5 \text{ V to } \pm 16 \text{ V})$ 

Supply current  $\pm 75$  mA typ. (depends on operating conditions,

recommended power supply capability min. ±150 mA)

Case Weight 0.32 kg (0.7 lbs)

Material AlMg4.5Mn, nickel-plated

Temperature Range Storage temperature -40 °C to +85 °C

Operating temperature 0 °C to +60 °C

Absolute Maximum Ratings Power supply voltage ±21 V

 $\begin{array}{lll} \mbox{Control input voltage} & +16\mbox{ V}/-5\mbox{ V} \\ \mbox{Signal input voltage} & \pm 0.7\mbox{ V} \\ \mbox{Input current} & \pm 25\mbox{ mA} \\ \end{array}$ 

Overvoltage at the signal input can severely degrade the noise performance

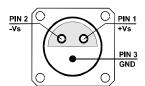
or destroy the amplifier!

Connectors Input BNC jack (female)
Output BNC jack (female)

Power supply Lemo® series 1S, 3-pin fixed socket

(mating plug type: FFA.1S.303.CLAC52)

Pin 1: +15V Pin 2: -15V Pin 3: GND



Control port Sub-D 25-pin, female

Pin 1: +12 V (stabilized power supply output,

max. 100 mA\*)

Pin 2: -12 V (stabilized power supply output,

max. 100 mA\*)

Pin 3: AGND (analog ground)

Pin 4: +5 V (stabilized power supply output,

max. 50 mA\*)

Pin 5: digital output: overload

Pin 6: NC Pin 7: NC

Pin 8: offset control voltage input

Pin 9: DGND (ground f. digital control Pin 10 - 25)

Pin 10: NC

Pin 11: digital control input: gain, LSB
Pin 12: digital control input: gain, MSB
Pin 13: digital control input: AC/DC
Pin 14: digital control input: 100 M/s / 1

Pin 14: digital control input: 100 kHz / 1 kHz

Pin 15 - 25: NC

\*check power supply for maximum deliverable current

## Low-Noise Variable Gain Low-Frequency Voltage Amplifier

Remote Control Operation General Remote control input bits are opto-isolated and connected by logical OR to local switch setting. For remote control set the corresponding local switch to "0 dB", "AC" and "1 kHz" and select the wanted setting via a bit-code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote controlled bandwidth setting, is also possible. Pin 11 Pin 12 Gain setting Gain 40 dB low low 60 dB high low 80 dB low high 100 dB high high AC/DC setting Coupling Pin 13 AC low DC high Bandwidth setting Bandwidth Pin 14 1 kHz low 100 kHz high Typical Performance Frequency response (logarithmic) Characteristics 110 100 90 80 Gain (dB) 60 50 40 20 Frequency (Hz) DG01-0444-17\_R1

# Low-Noise Variable Gain Low-Frequency Voltage Amplifier

157 mm

150 mm

137 mm

OVERIOAD
OUT

AC

100 MHz

POWER

86 mm

107 mm

107 mm