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### **Datasheet**

### **DLPVA-100-BUN-S**

## Ultra-Low-Noise Variable Gain Low-Frequency Voltage Amplifier



Features	<ul> <li>Variable gain 40 to 100 dB, switchable in 20 dB steps</li> <li>Bipolar input stage, recommended for low impedance sources smaller than 50 Ω</li> <li>Ultra low input voltage noise: 400 pV/√Hz</li> <li>AC coupled, single ended</li> <li>Bandwidth 1.5 Hz - 100 kHz, switchable to 1 kHz</li> <li>Local and remote control</li> </ul>
Applications	<ul> <li>Ultra low-noise laboratory amplifier</li> <li>Pulsed thermal EMF analysis</li> <li>Chopped thermopiles / bolometers</li> <li>Industrial sensors</li> <li>Detector preamplifier</li> <li>Integrated measurement systems</li> </ul>
Block Diagram	Ultra Low-Noise Input Stage AC Coupling Programmable Gain Amplifier Gain Amplifier F. 3dB = 1k/100 Hz    NPUT
	Parameter Control Unit  Supply Voltage Regulator  IN (+/-15V)  Optocoupler Isolate Unit  DIG. CONTROL INPUTS  DIG. CONTROL INPUTS  POWER SUPPLY OUTPUT

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

## **Ultra-Low-Noise Variable Gain Low-Frequency Voltage Amplifier**

Specifications Test conditions  $V_s = \pm 15 \text{ V}, T_A = 25 \text{ °C}, \text{ load impedance} = 1 \text{ M}\Omega$ 

40, 60, 80, 100 dB Gain Gain values

indicated by four LEDs

Gain accuracy ±1 %

Lower cut-off frequency Frequency Response 1.5 Hz

> Upper cut-off frequency 100 kHz, 12 dB/Oct

> > switchable to 1 kHz, 6 dB/Oct.

Time Response Rise/fall time (10 % - 90 %)  $3.5 \mu s$  (@ BW = 100 kHz)

350  $\mu$ s (@ BW = 1 kHz)

Input Input impedance 1 k $\Omega$  tvp.

> Equivalent input voltage noise Gain setting Noise

> > 100 dB 400 pV/√Hz 420 pV/√Hz 80 dB 800 pV/√Hz 60 dB 6 nV/√Hz 40 dB

Equivalent input current noise 5,5 pA/√Hz 100 Hz 1/f-noise corner Input bias current 30 μΑ

Maximum input DC-offset voltage

for linear amplification ±90 mV

**Important notice:** The input must see a source impedance below 200  $\Omega$ .

Output Output impedance <100  $\Omega$  (terminate with > 100 k $\Omega$  load for best

performance)

Output voltage range

for linear amplification  $\pm 10 \text{ V } (@ > 100 \text{ k}Ω \text{ load})$ 

Output current (max.) ±20 mA

Output overload recovery time 0.5 ms (after 20x overload)

Overload LED The amplifier features a LED to signalize an overload condition. The Overload LED will turn on if the signal level within the signal path exceeds the linear operating range. In order to ensure

the correct operation of the amplifier without signal distortions reduce the gain setting until

the Overload LED turns off.

The Overload LED may also turn on when the amplifier is operated with open input or with a high source impedance, e. g. external AC coupling. In this case the bias current may cause a considerable input voltage. For proper operation please use a source impedance of less than

100  $\Omega$  or switch to a lower gain setting.

Remote Digital Control Control input voltage range Low: -0.8 ...+0.8 V

High: +1.8 ... +12 V, TTL / CMOS compatible

Control input current 0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V

Overload output Non active: +5 V, max. 1 mA, active: 0.8 V, max. -10 mA

**Power Supply** Supply voltage  $\pm 15 \text{ V} (\pm 14.5 \text{ V to } \pm 16 \text{ V})$ 

> Supply current ±55 mA typ. (depends on operating conditions,

recommended power supply capability min. ±150 mA)

## Ultra-Low-Noise Variable Gain Low-Frequency Voltage Amplifier

Specifications (continued)

Case

Weight 0.32 kg (0.7 lbs)

Material AlMg4.5Mn, nickel-plated

Temperature Range Storage temperature

−40 °C to +70 °C

Operating temperature

0 °C to +55 °C

Absolute Maximum Ratings

Power supply voltage Control input voltage ±21 V +16 V / -5 V

Signal input voltage

Overvoltage at the signal input can severely degrade the noise performance

+4 V

or destroy the amplifier!

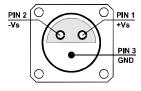
Connectors

Input Output BNC jack (female) BNC jack (female)

Power supply Lemo® series 1S, 3-pin fixed socket

(mating plug type: FFA.1S.303.CLAC52)

Pin 1: +15V Pin 2: -15V Pin 3: GND



Control port

Sub-D 25-pin, female

Pin 1: +12 V (stabilized power supply output,

max. 60 mA\*)

Pin 2: —12 V (stabilized power supply output,

max. 60 mA\*)

Pin 3: AGND (analog ground)

Pin 4: +5 V (stabilized power supply output,

max. 15 mA\*)

Pin 5: digital output: overload

Pin 6: NC Pin 7: NC Pin 8: NC

Pin 9: DGND (ground f. digital control Pin 10 - 25)

Pin 10: NC

Pin 11: digital control input: gain, LSB Pin 12: digital control input: gain, MSB

Pin 13: NC

Pin 14: digital control input: 100 kHz / 1 kHz

Pin 15 - 25: NC

\*check power supply for maximum deliverable current

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Remote Control Operation General Remote control input bits are opto-isolated and connected by logical OR to local switch setting. For remote control set the corresponding local switch to "0 dB" and "1 kHz" and select the wanted setting via a bit-code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote controlled bandwidth setting, is also possible. Pin 11 Pin 12 Gain setting Gain 40 dB low low 60 dB high low 80 dB high low high 100 dB high Bandwidth setting Bandwidth Pin 14 1 kHz low 100 kHz high Typical Performance Frequency response Characteristics 110 100 90 80 Gain (dB) 70 60 50 40 30 20 10 100 10 k 100 k 1 k 1 M Frequency (Hz) Frequency response at switch setting 100 kHz Frequency response at switch setting 1 kHz

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Dimensions

