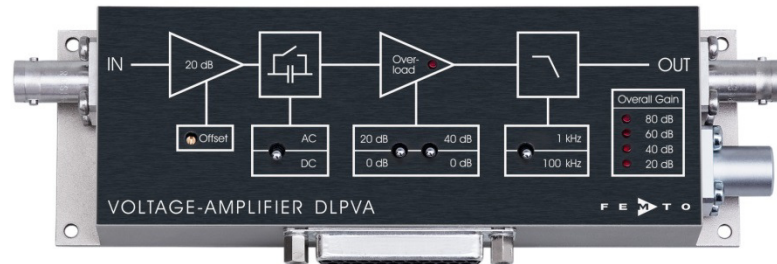


Datasheet

DLPVA-100-F Series

Variable Gain Low-Frequency Voltage Amplifier

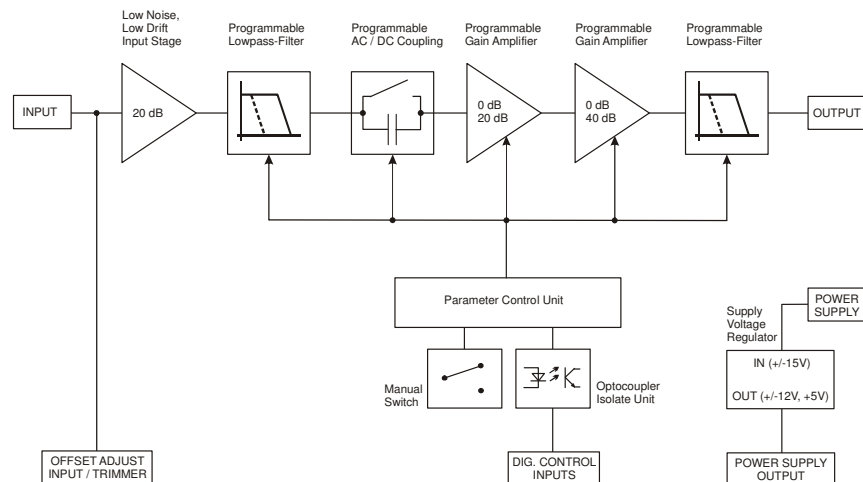


Features

- **Variable gain 20 to 80 dB, switchable in 20 dB steps**
- **FET input stage, 1 TΩ impedance**
- **Protection against ±3 kV transients**
- **Single ended and true differential input models**
- **Bandwidth DC - 100 kHz, switchable to 1 kHz**
- **1.3 μV/°C DC-drift**
- **120 dB CMRR**
- **5.5 nV/√Hz input noise**
- **Switchable AC/DC-coupling**
- **Local and remote control**

Applications

- **Universal laboratory amplifier**
- **Automated measurements**
- **Industrial sensors**
- **Detector preamplifier**
- **Integrated measurement systems**



BS01-0440-20_20dB

Variable Gain Low-Frequency Voltage Amplifier

Specifications	Test conditions	$V_s = \pm 15\text{ V}$, $T_A = 25\text{ }^\circ\text{C}$, load impedance = 1 M Ω		
Gain	Gain values	20, 40, 60, 80 dB Indicated by four LEDs		
	Gain accuracy	$\pm 0.1\%$ (between settings) $\pm 1\%$ (overall)		
	Gain flatness	$\pm 0.1\text{ dB}$		
Frequency Response	Lower cut-off frequency	DC, switchable to 1.5 Hz		
	Upper cut-off frequency	100 kHz, switchable to 1 kHz		
	Upper cut-off frequency rolloff	12 dB/oct.		
Time Response	Rise/fall time (10% - 90%)	3.5 μs (@ BW = 100 kHz)		
		350 μs (@ BW = 1 kHz)		
Input	Input impedance	1 T Ω		
	Input capacitance	18 pF		
	Input voltage drift	1.3 $\mu\text{V}/^\circ\text{C}$		
	Equivalent input voltage noise	Gain setting	DLPVA-100-F-S	DLPVA-100-F-D
		60, 80 dB	5.5 nV/ $\sqrt{\text{Hz}}$	6.9 nV/ $\sqrt{\text{Hz}}$
		40 dB	8 nV/ $\sqrt{\text{Hz}}$	10 nV/ $\sqrt{\text{Hz}}$
		20 dB	60 nV/ $\sqrt{\text{Hz}}$	60 nV/ $\sqrt{\text{Hz}}$
		Equivalent input current noise	1.6 fA/ $\sqrt{\text{Hz}}$	
	1/f-Noise corner	80 Hz		
	Input bias current	1 pA		
	Input bias current drift	Factor 2.3 / 10 $^\circ\text{C}$		
	Input offset voltage	$\pm 5\text{ mV}$, adjustable by offset trimmer and external control voltage		
	<i>True differential input, model "DLPVA-100-F-D" only:</i>			
Common mode voltage range	$\pm 8\text{ V}$			
CMRR	120 dB (@ 100 Hz)			
	100 dB (@ 10 kHz)			
	80 dB (@ 60 kHz)			
Output	Output impedance	<100 Ω (terminate with > 10 k Ω load for best performance)		
	Output voltage range	$\pm 10\text{ V}$ (@ > 10 k Ω load)		
	For linear amplification	$\pm 20\text{ mA}$		
	Output current (max.)	$\pm 20\text{ mA}$		
	Output overload recovery time	0.5 ms (after 20x overload)		

Variable Gain Low-Frequency Voltage Amplifier

Specifications (continued)

<p>Overload LED</p>	<p>The amplifier features a LED to indicate an overload condition. The Overload LED will turn on if the signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until the Overload LED turns off.</p> <p>The Overload LED may also turn on under the following operating conditions:</p> <ul style="list-style-type: none"> - The amplifier is operated with open input or with a high source resistance, e. g. external AC coupling. Due to the near infinite input resistance a charge present at the input will persist. For proper operation please use a source resistance of less than 100 MΩ or switch to a lower gain setting. - When using a DLPVA-F-D with differential input stage the Overload LED may turn on if the common mode input voltage exceeds the common mode voltage range. This is likely to happen when the source is floating with respect to the amplifier ground. For proper operation make sure that the common mode voltage stays within the allowed common mode voltage range with respect to the amplifier ground. Provide an electrical connection between the source ground and the amplifier ground to ensure the inputs cannot drift outside the tolerable common mode range. 								
<p>Remote Offset Control</p>	<table data-bbox="550 869 1315 934"> <tr> <td>Offset control voltage range</td> <td>± 10 V, corresponds to ± 5 mV input offset voltage</td> </tr> <tr> <td>Offset control input impedance</td> <td>200 kΩ</td> </tr> </table>	Offset control voltage range	± 10 V, corresponds to ± 5 mV input offset voltage	Offset control input impedance	200 k Ω				
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<p>Remote Digital Control</p>	<table data-bbox="550 955 1388 1081"> <tr> <td>Control input voltage range</td> <td>Low: -0.8 ... +0.8 V High: +1.8 ... +12 V, TTL / CMOS compatible</td> </tr> <tr> <td>Control input current</td> <td>0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V</td> </tr> <tr> <td>Overload output</td> <td>Non active: +5 V, max. 1 mA, active: 0.8 V, max. -10 mA</td> </tr> </table>	Control input voltage range	Low: -0.8 ... +0.8 V High: +1.8 ... +12 V, TTL / CMOS compatible	Control input current	0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V	Overload output	Non active: +5 V, max. 1 mA, active: 0.8 V, max. -10 mA		
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<p>Power Supply</p>	<table data-bbox="550 1106 1356 1207"> <tr> <td>Supply voltage</td> <td>± 15 V (± 14.5 V to ± 16 V)</td> </tr> <tr> <td>Supply current</td> <td>± 75 mA typ. (depends on operating conditions, recommended power supply capability min. ± 150 mA)</td> </tr> </table>	Supply voltage	± 15 V (± 14.5 V to ± 16 V)	Supply current	± 75 mA typ. (depends on operating conditions, recommended power supply capability min. ± 150 mA)				
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<p>Case</p>	<table data-bbox="550 1232 1096 1291"> <tr> <td>Weight</td> <td>0.32 kg (0.7 lbs)</td> </tr> <tr> <td>Material</td> <td>AlMg4.5Mn, nickel-plated</td> </tr> </table>	Weight	0.32 kg (0.7 lbs)	Material	AlMg4.5Mn, nickel-plated				
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<p>Temperature Range</p>	<table data-bbox="550 1318 1039 1381"> <tr> <td>Storage temperature</td> <td>-40 $^{\circ}$C to $+85$ $^{\circ}$C</td> </tr> <tr> <td>Operating temperature</td> <td>0 $^{\circ}$C to -60 $^{\circ}$C</td> </tr> </table>	Storage temperature	-40 $^{\circ}$ C to $+85$ $^{\circ}$ C	Operating temperature	0 $^{\circ}$ C to -60 $^{\circ}$ C				
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<p>Absolute Maximum Ratings</p>	<table data-bbox="550 1417 1185 1554"> <tr> <td>Power supply voltage</td> <td>± 21 V</td> </tr> <tr> <td>Control input voltage</td> <td>$+16$ V / -5 V</td> </tr> <tr> <td>Signal input voltage</td> <td>± 15 Vp</td> </tr> <tr> <td>Transient input voltage</td> <td>± 3 kV (discharge from 5 nF source)</td> </tr> </table>	Power supply voltage	± 21 V	Control input voltage	$+16$ V / -5 V	Signal input voltage	± 15 Vp	Transient input voltage	± 3 kV (discharge from 5 nF source)
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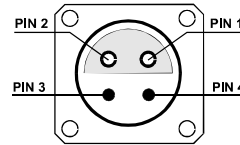
Variable Gain Low-Frequency Voltage Amplifier

Connectors

Input

Single ended input, model "DLPVA-100-F-S":
BNC jack (female)

True differential input, model "DLPVA-100-F-D":
Lemo® series 1S, 4-pin fixed socket
(mating plug type: FFA.1S.304.CLAC52)
Pin 1: non inverting input
Pin 2: inverting input
Pin 3: GND
Pin 4: NC

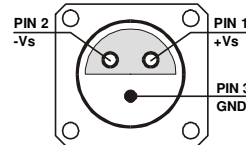


Output

BNC jack (female)

Power supply

Lemo® series 1S, 3-pin fixed socket
(mating plug type: FFA.1S.303.CLAC52)
Pin 1: +15V
Pin 2: -15V
Pin 3: GND



Control port

Sub-D 25-pin, female
Pin 1: +12 V (stabilized power supply output, max. 100 mA*)
Pin 2: -12 V (stabilized power supply output, max. 100 mA*)
Pin 3: AGND (analog ground)
Pin 4: +5 V (stabilized power supply output, max. 50 mA*)
Pin 5: digital output: overload
Pin 6: NC
Pin 7: NC
Pin 8: offset control voltage input
Pin 9: DGND (ground f. digital control Pin 10 - 25)
Pin 10: NC
Pin 11: digital control input: gain, LSB
Pin 12: digital control input: gain, MSB
Pin 13: digital control input: AC/DC
Pin 14: digital control input: 100 kHz / 1 kHz
Pin 15 - 25: NC

*check power supply for maximum deliverable current

Variable Gain Low-Frequency Voltage Amplifier

Remote Control Operation

General

Remote control input bits are opto-isolated and connected by logical OR to local switch setting.

For remote control set the corresponding local switch to "0 dB", "AC" and "1 kHz" and select the wanted setting via a bit-code at the corresponding digital inputs.

Mixed operation, e.g. local gain setting and remote controlled bandwidth setting, is also possible.

Gain setting

Gain	Pin 11	Pin 12
20 dB	low	low
40 dB	high	low
60 dB	low	high
80 dB	high	high

AC/DC setting

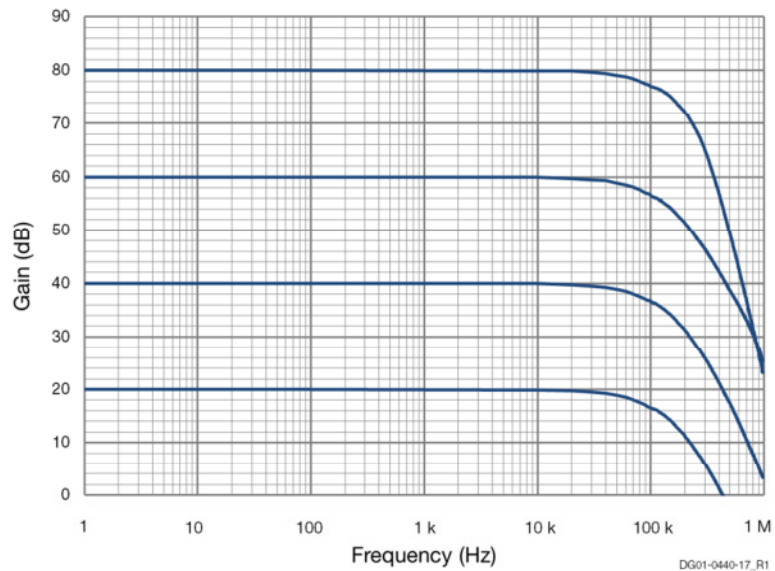
Coupling	Pin 13
AC	low
DC	high

Bandwidth setting

Bandwidth	Pin 14
1 kHz	low
100 kHz	high

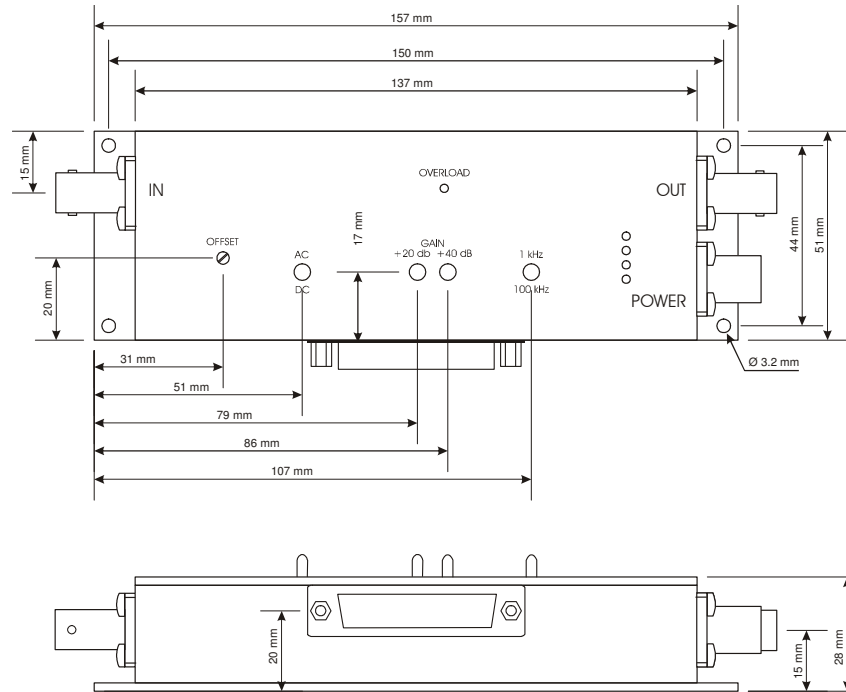
Typical Performance Characteristics

Frequency response (logarithmic)



Variable Gain Low-Frequency Voltage Amplifier

Dimensions



DZ-DLPVA-100_R1

Model no.: DLPVA-100-F-S (DLPVA-100-F-D differs regarding input connector)

Ordering Information

Available models

- Model No.: DLPVA-100-F-S
- FET, single-ended input (BNC-connector input)
- Model No.: DLPVA-100-F-D
- FET, true differential input (Lemo®-connector input)