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Datasheet DLPVA-101-B

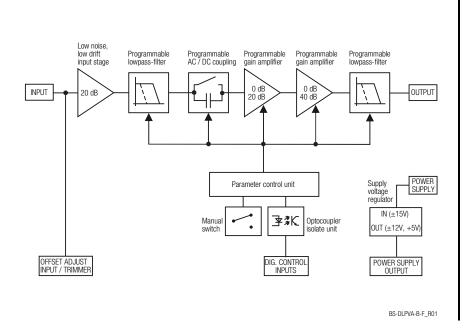
Variable Gain Low-Frequency Voltage Amplifier



The picture shows model DLPVA-101-B-S with BNC input

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Features	 Variable gain 20 to 80 dB, switchable in 20 dB steps Bipolar input stage, recommended for low impedance sources less than 1 kΩ Single ended and true differential input models Bandwidth DC – 100 kHz, switchable to 1 kHz 0.7 μV/°C DC-drift 120 dB CMRR Down to 2.0 nV/√Hz input noise Switchable AC/DC-coupling Local and remote control
Applications	 Universal laboratory amplifier Automated measurements Industrial sensors Detector preamplifier Integrated measurement systems

Block Diagram



Datasheet DLPVA-101-B Variable Gain Low-Frequency Voltage Amplifier Intended Use The DLPVA-101-B voltage amplifiers are variable gain voltage amplifiers. They are designed for fast amplification of small voltage signals. Operation is largely self-explanatory. If in doubt, consult this document or contact support@femto.de. For safe operation, please refer to the damage thresholds specified in the "Absolute Maximum" Ratings", "Temperature Range" and "Power Supply" sections of this document. The operating environment must be free of smoke, dust, grease, oil, condensing moisture, and other contaminants that could affect the operation or performance. **Application Notes** The DLPVA-101-B amplifiers are designed for use with low resistance sources. A high source resistance causes significant increase of the input offset voltage and may trigger overload status. See "Overload LED" section for details. When using a DLPVA-101-B-D with differential input, ensure that the common mode voltage. relative to the amplifier case, does not exceed the allowable range of ±8 V. A floating source, such as an induction coil, without any connection to the amplifier ground will trigger the overload status as well. DLPVA-101-B-S Variable gain voltage amplifier, gain settings 20/40/60/80 dB, **Available Versions** single ended (bipolar), typical source resistance <1 k Ω , input 1 M Ω (BNC), bandwidth DC/1.5 Hz – 1/100 kHz DLPVA-101-B-D Variable gain voltage amplifier, gain settings 20/40/60/80 dB, true differential (bipolar), typical source resistance $<10 \text{ k}\Omega$, input 1 M Ω (LEMO®), bandwidth DC/1.5 Hz – 1/100 kHz Related Models DLPVA-101-BLN-S Variable gain voltage amplifier, gain settings 40/60/80/100 dB, single ended (bipolar), typical source resistance <100 Ω , input 1 M Ω (BNC), bandwidth DC/1.5 Hz – 1/100 kHz DLPVA-101-F-S Variable gain voltage amplifier, gain settings 20/40/60/80 dB, single ended (FET), typical source resistance <1 M Ω , input 1 T Ω (BNC), bandwidth DC/1.5 Hz – 1/100 kHz Variable gain voltage amplifier, gain settings 20/40/60/80 dB, DLPVA-101-F-D true differential (FET), typical source resistance <1 M Ω . input 1 T Ω (LEMO®), bandwidth DC/1.5 Hz – 1/100 kHz DLPVA-100-BUN-S Ultra-low-noise variable gain voltage amplifier, gain settings 40/60/80/100 dB, single ended (bipolar), typical source resistance <50 Ω , input 1 k Ω (BNC), bandwidth 1.5 Hz - 1/100 kHz Available Accessories PS-15-25-L **Power Supply** Input: AC 100 - 240 V Output: DC ±15 V LUCI-10 Compact digital I/O interface for USB remote control, supports opto-isolation of amplifier signal path from PC USB port. 16 digital outputs, 3 opto-isolated digital inputs, bus-powered operation

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Specifications $V_S = \pm 15 \text{ V}, T_A = 25 \text{ °C}, \text{ output load impedance 1 M}\Omega,$

warm-up 20 minutes (min. 10 minutes recommended)

Gain Gain values 20, 40, 60, 80 dB, indicated by LEDs, (@ output load ≥100 kΩ)

Gain accuracy $\pm 0.05 \text{ dB}$

Frequency Response Lower cut-off frequency DC / 1.5 Hz, switchable

Upper cut-off frequency (-3 dB) 100 kHz / 1 KHz, switchable

Upper cut-off frequency roll-off 12 dB/oct.

Time Response Rise/fall time (10 % - 90 %) 3.5 µs (@ bandwidth 100 kHz)

350 µs (@ bandwidth 1 kHz)

Input impedance 1 MΩ II 105 pF

Input voltage drift $0.7 \mu V/^{\circ}C$

Equ. input noise voltage gain settings DLPVA-101-B-S DLPVA-101-B-D

20 dB 5.0 nV/ $\sqrt{\text{Hz}}$ 5.0 nV/ $\sqrt{\text{Hz}}$ 40, 60, 80 dB 2.0 nV/ $\sqrt{\text{Hz}}$ 3.0 nV/ $\sqrt{\text{Hz}}$

Equ. input noise current 2 pA/ $\sqrt{\text{Hz}}$ 1/f-noise corner 80 Hz Input bias current 0.8 μ A Input bias current drift 6 nA/°C

Input offset voltage ± 4 mV, adjustable by offset trimmer and external contr. voltage

True differential input, model "DLPVA-101-B-D" only:

Common mode voltage range ±8 V

CMRR 120 dB (@ 100 Hz)

100 dB (@ 10 kHz) 80 dB (@ 60 kHz)

Output Output voltage range $\pm 10 \text{ V}$ (@ $\geq 100 \text{ k}\Omega$ output load)

Output impedance 50 Ω (terminate with \geq 100 k Ω load for best performance)

 $\begin{array}{ll} \text{Max. output current} & \pm 20 \text{ mA (short-circuit proof)} \\ \text{Output overload recovery time} & 0.5 \text{ ms (after 20 x overload)} \end{array}$

Overload LED

The amplifier features a LED to indicate an overload condition. The Overload LED will turn on if the signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until the Overload LED

The Overload LED may also turn on under the following operating conditions:

- The amplifier is operated with open input or with a high source resistance, e. g. external AC coupling. In this case the bias current may cause a considerable input voltage. For proper operation please use a source resistance of less than 1 k Ω for model DLPVA-101-B-S and less than 10 k Ω for model DLPVA-101-B-D, respectively, or switch to a lower gain setting.
- When using a DLPVA-101-B-D with differential input stage the Overload LED may turn on if the common mode input voltage exceeds the common mode voltage range. This is likely to happen when the source is floating with respect to the amplifier ground. For proper operation make sure that the common mode voltage stays within the allowed common mode voltage range with respect to the amplifier ground. Provide an electrical connection between the source ground and the amplifier ground to ensure the inputs cannot drift outside the tolerable common mode range.

Digital Control Control input voltage range Low: -0.8 ...+0.8 V

Overload output

High: +1.8 ... +12 V, TTL / CMOS compatible 0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V Non active: +5 V, max. 1 mA, active: 0.8 V, max. -10 mA

High: +1.8 ... +12 V, TTL / CMOS compatible

Control input current

O mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12

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Specifications (continued)					
Ext. Offset Control	Offset control voltage range Offset control input impedance	± 10 V (+10 V corresponds to +4 mV input offset voltage) 200 $k\Omega$			
Power Supply	Supply voltage Supply current	DC ±15 V (±14.5 V to ±16 V) ±75 mA typ. (depends on operating conditions, recommended power supply capability min. ±150 mA)			
Case	Weight Material	320 g (0.7 lbs) AlMg4.5Mn, nickel-plated			
Temperature Range	Storage temperature Operating temperature	-40 °C +80 °C 0 °C +60 °C			
Absolute Maximum Ratings	Digital control input voltage Analog control input voltage Power supply voltage Model DLPVA-101-B-S only, sing Input voltage Model DLPVA-101-B-D only, true Input differential voltage Input common mode voltage	±4.5 V			
Connectors	Input Common mode voltage	Model DLPVA-101-B-S			
	BNC jack (female) Model DLPVA-101-B-D LEMO® series 1S, 4-pin fixed socket (mating plug type: FFA.1S.304.CLAC52)				
		PIN 2 IN PIN 3 PIN 4 PIN 4 RND PIN 4 PIN 4: not connected (NC)			
	Output Power supply	BNC jack (female) LEMO® series 1S, 3-pin fixed socket (mating plug type: FFA.1S.303.CLAC52)			
		PIN 2 -V _s PIN 1 +V _s Pin 1: +15 V Pin 2: -15 V Pin 3: ground (GND)			

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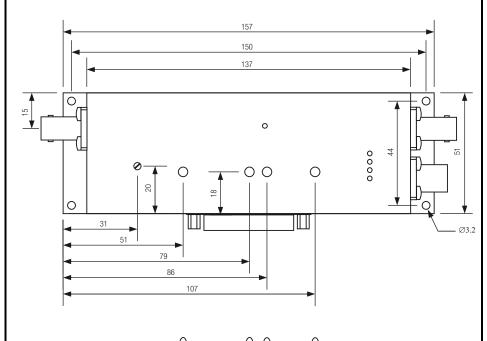
Connectors (continued)	Control port	Sub-D 25-pin, female, qual. class 2				
		$\begin{pmatrix} 13 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & $				
		Pin 1: +12 V (stabilized power supply output*) Pin 2: -12 V (stabilized power supply output*) Pin 3: AGND (analog ground for pins 1 – 8) Pin 4: +5 V (stabilized power supply output*) Pin 5: digital output: overload (referred to pin 3) Pin 6: NC Pin 7: NC Pin 8: input offset control voltage Pin 9: DGND (ground for digital control pins 10 – 14) Pin 10: NC Pin 11: digital control input: gain, LSB Pin 12: digital control input: gain, MSB Pin 13: digital control input: AC/DC Pin 14: digital control input: 100kHz / 1 kHz Pin 15 – 25: NC				
		±12 V: max.	*stabilized power supply output current ±12 V: max. ±100 mA +5V: max. 50 mA			
Remote Control Operation	General	by logical Of control set tl and "1 kHz" via a bit cod	Remote control input bits are opto-isolated and connected by logical OR function to local switch settings. For remote control set the corresponding local switches to "0 dB", "AC" and "1 kHz" and select the wanted setting via a bit code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote			
		controlled bandwidth setting, is also possible.				
	Gain setting	Gain	Pin 11 LSB	Pin 12 MSB		
		20 dB 40 dB 60 dB 80 dB	low high low high	low low high high		
	AC/DC setting	Coupling	Pin 13			
		AC DC	low high			
	Bandwidth setting	Bandwidth	Pin 14			
		1 kHz 100 kHz	low high			
Scope of Delivery	DLPVA-101-B, LEMO® 3-pin connector, LEMO® 4-pin connector (model DLPVA-101-B-D only), datasheet, transport package					
Ordering Information	DLPVA-101-B-S DLPVA-101-B-D	Variable gain voltage amplifier, single ended (bipolar) Variable gain voltage amplifier, true differential (bipolar)				

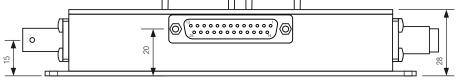
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all dimensions in mm unless otherwise noted

Dimensions continued

DLPVA-101-B-S





DZ-DLPVA-101-BLN-B-F-S_R01

all dimensions in mm unless otherwise noted

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